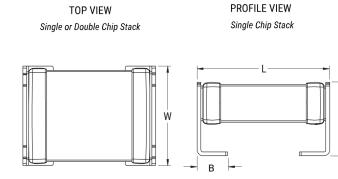


## C1812C105M1R1C7186

KPS SMD Comm X7R, Ceramic, 1 uF, 20%, 100 VDC, X7R, 1812-1



General Information	
Series	KPS SMD Comm X7R
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Single Chip, Temperature Stable
Features	Temperature Stable
RoHS	Yes
Termination	Tin
AEC-Q200	No
Component Weight	170 mg
Chip Size	1812-1
Shelf Life	78 Weeks
MSL	1

Click here for the 3D model.

L 5	5mm +/-0.5mm
W 3	3.5mm +/-0.5mm
Т 2	2.65mm +/-0.35mm
В 1.	.1mm +/-0.3mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	500

Specifications	
Capacitance	1uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	20%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	1GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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