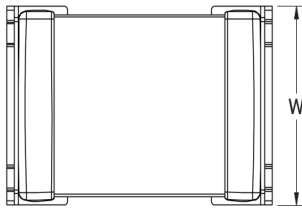


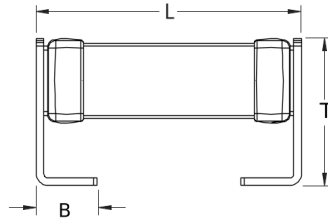
TOP VIEW

Single or Double Chip Stack



PROFILE VIEW

Single Chip Stack



### General Information

|                  |   |
|------------------|---|
| Series           | KPS SMD Comm X7R                                    |
| Style            | Stacked Chip  |
| Description      | SMD, MLCC, Stacked, Single Chip, Temperature Stable |
| Features         | Temperature Stable                                  |
| RoHS             | Yes   |
| Termination      | Tin   |
| AEC-Q200         | No  |
| Component Weight | 170 mg  |
| Chip Size        | 1812-1  |
| Shelf Life       | 78 Weeks  |
| MSL              | 1   |

### Specifications

|  |   |
|--|---|
| Capacitance  | 1 uF  |
| Measurement Condition  | 1 kHz 1.0Vrms                                   |
| Capacitance Tolerance  | 20%   |
| Voltage DC   | 100 VDC   |
| Dielectric Withstanding Voltage                                    | 250 VDC   |
| Temperature Range  | -55/+125°C                                      |
| Temperature Coefficient  | X7R   |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms                               |
| Dissipation Factor   | 2.5% 1kHz 1.0Vrms                               |
| Aging Rate   | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance  | 1 GOhms   |

Click [here](#) for the 3D model.

### Dimensions

|   |                  |
|---|------------------|
| L | 5mm +/-0.5mm     |
| W | 3.5mm +/-0.5mm   |
| T | 2.65mm +/-0.35mm |
| B | 1.1mm +/-0.3mm   |

### Packaging Specifications

|                    |                          |
|--------------------|--------------------------|
| Packaging          | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 500                      |