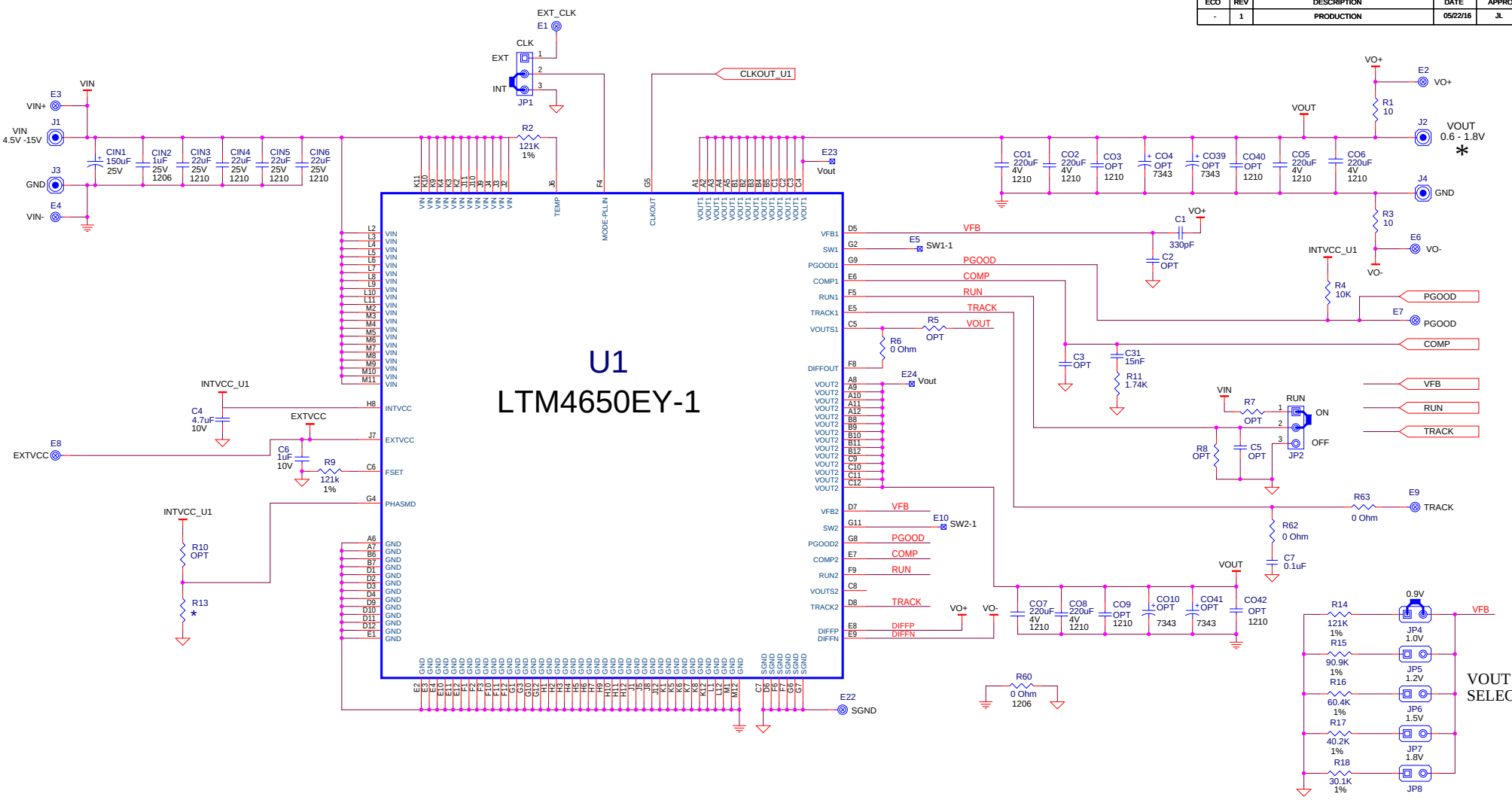


REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
-	1	PRODUCTION	05/22/16	JL

*								
ASSY	I.C.	Iout	R13	R34	Cin12, 13, 14, 15	Cin16, 17, 18, 19	Co20, 21,26,27 Co33,36,52,54	Co22,28,30,32 Co34,35,48,50
-A	U1, U2	100A	OPT	OPT	OPT	OPT	OPT	OPT
-B	U1, U2, U3	150A	0	0	22uF	OPT	220uF	OPT
-C	U1, U2, U3, U4	200A	OPT	0	22uF	22uF	220uF	220uF

NOTE: UNLESS OTHERWISE SPECIFIED,
1. ALL CAPACITORS, RESISTORS 0603.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	LT
APP ENG.	JL

SCALE = NONE

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035 www.linear.com
Phone: (408)432-1900
Fax: (408)434-0507

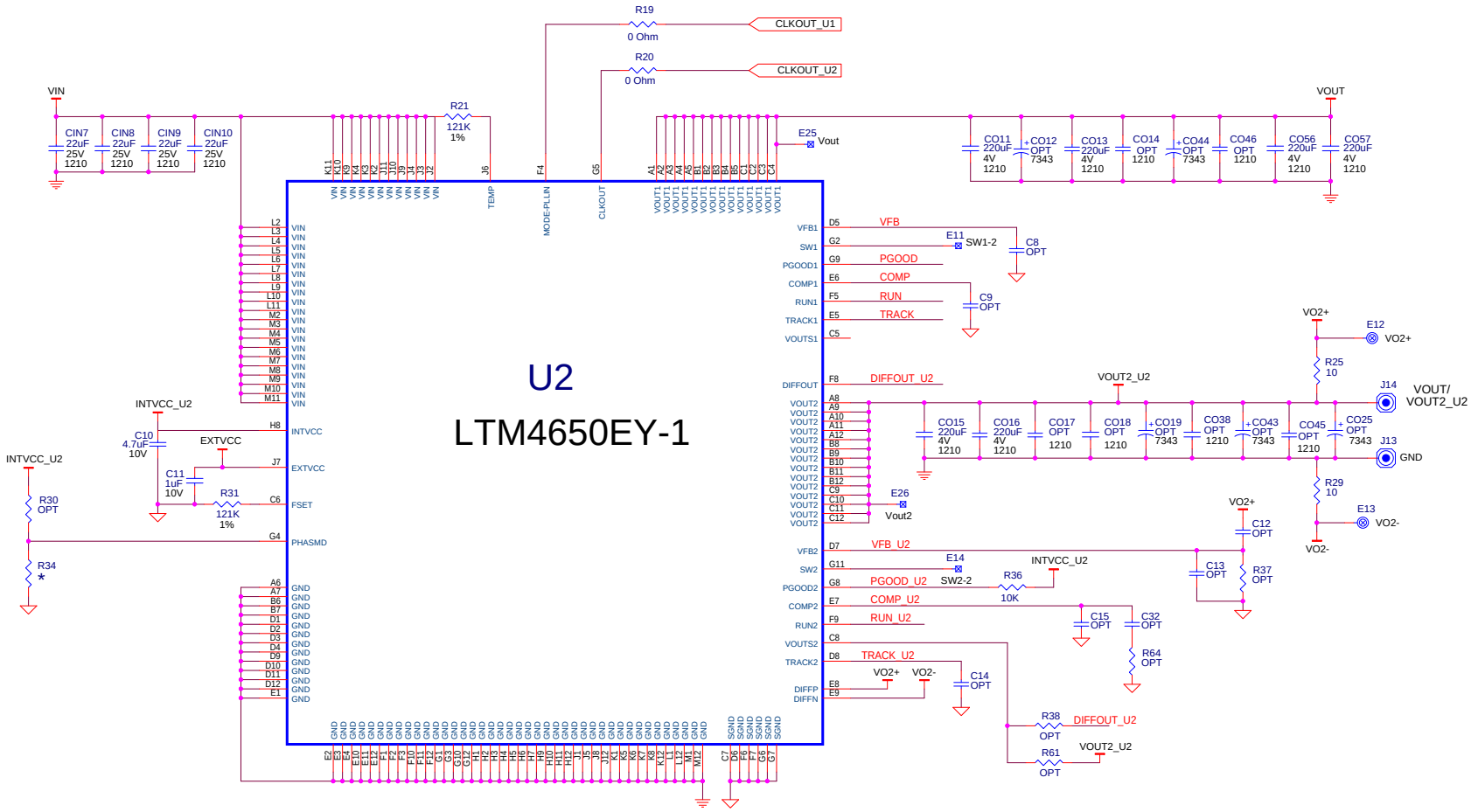
TITLE: SCHEMATIC

HIGH EFFICIENCY, POLYPHASE, DC/DC STEP-DOWN µMODULE REGULATOR

SIZE	IC NO.	REV.
N/A	LTM4650EY-1 DEMO CIRCUIT 2455A-(A,B,C)	1

DATE: Monday, May 23, 2016

SHEET 1 OF 5



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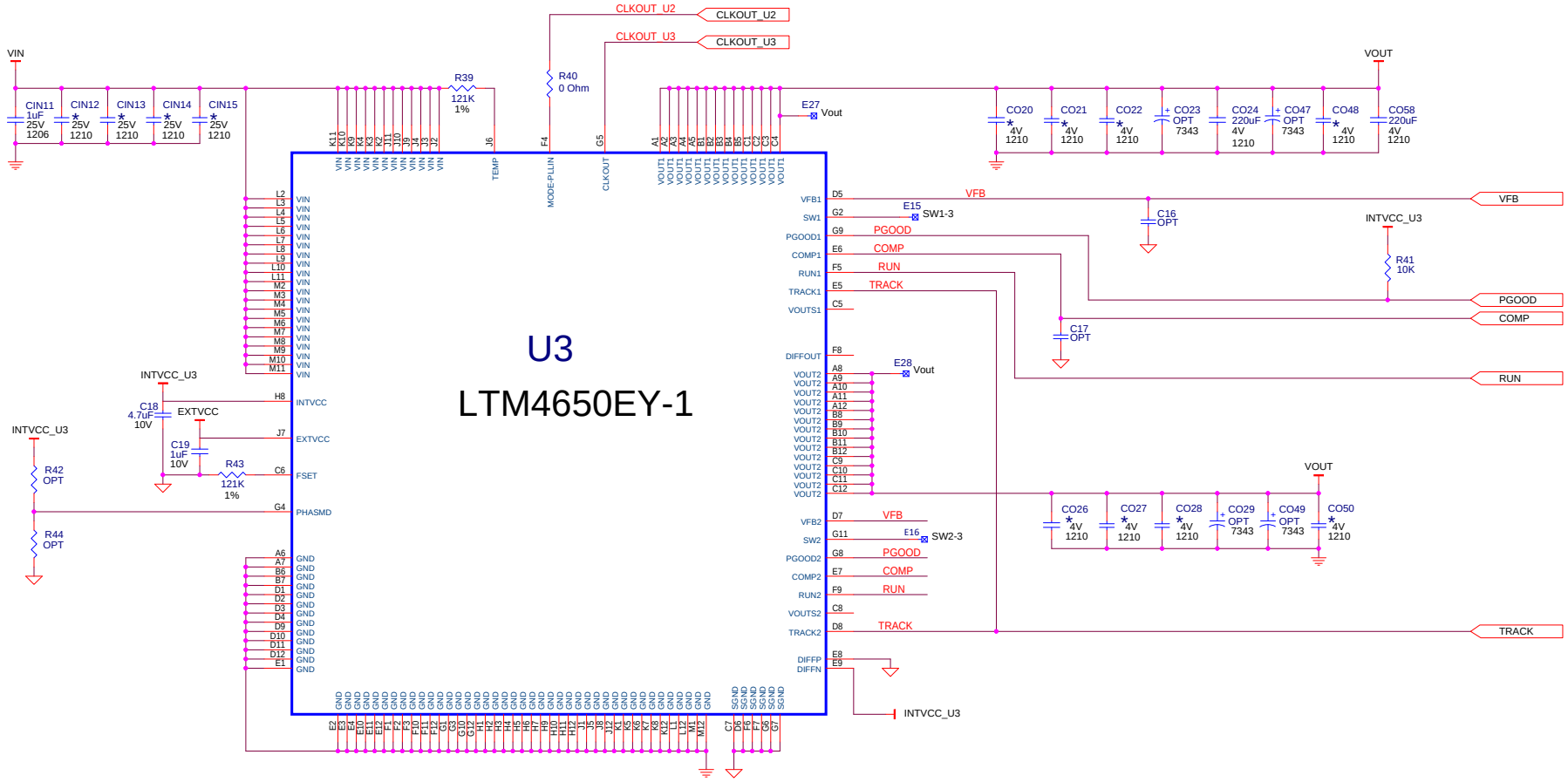

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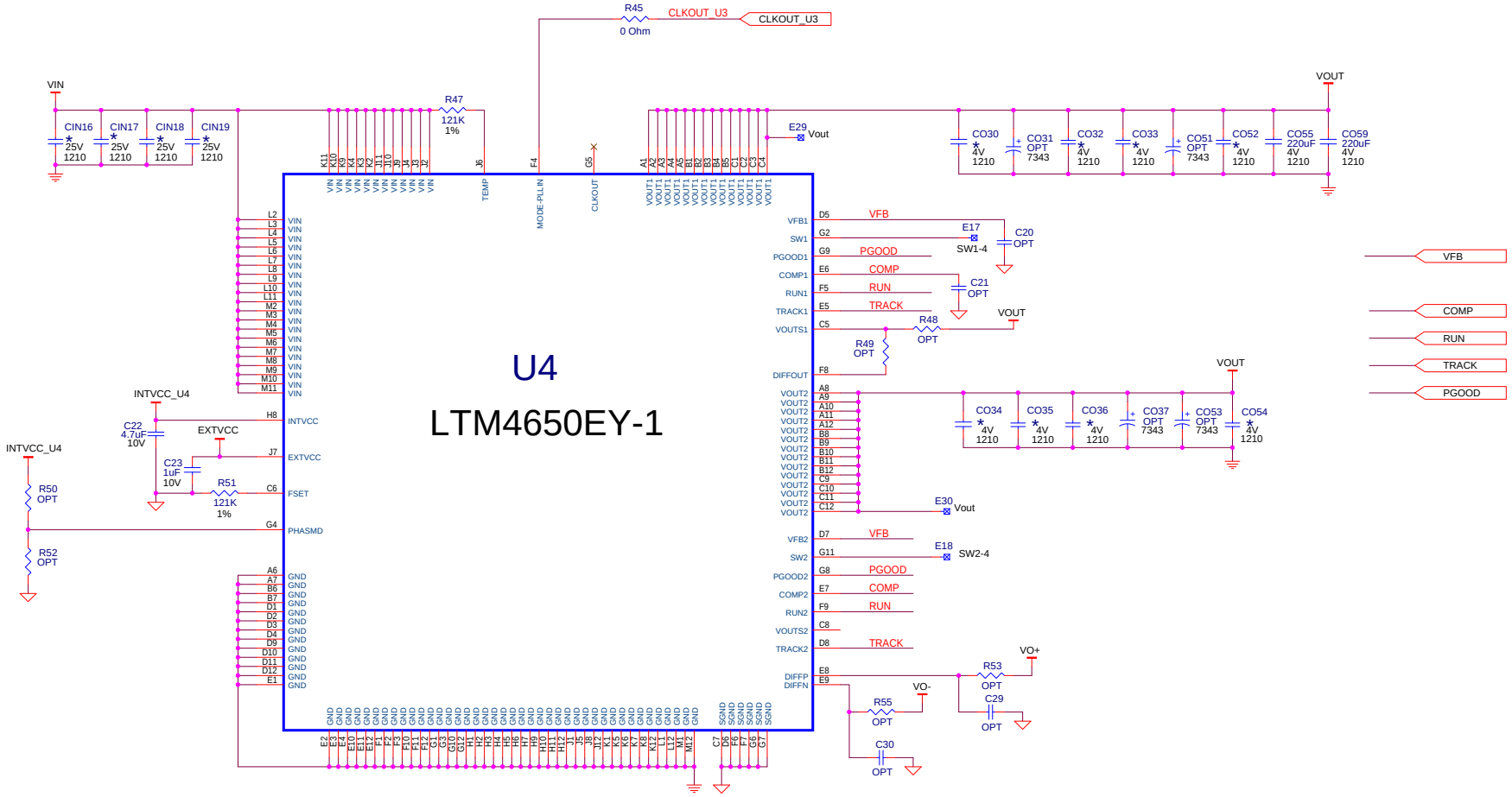

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N/A	LTM4650EY-1 DEMO CIRCUIT 2455A-(A,B,C)	1

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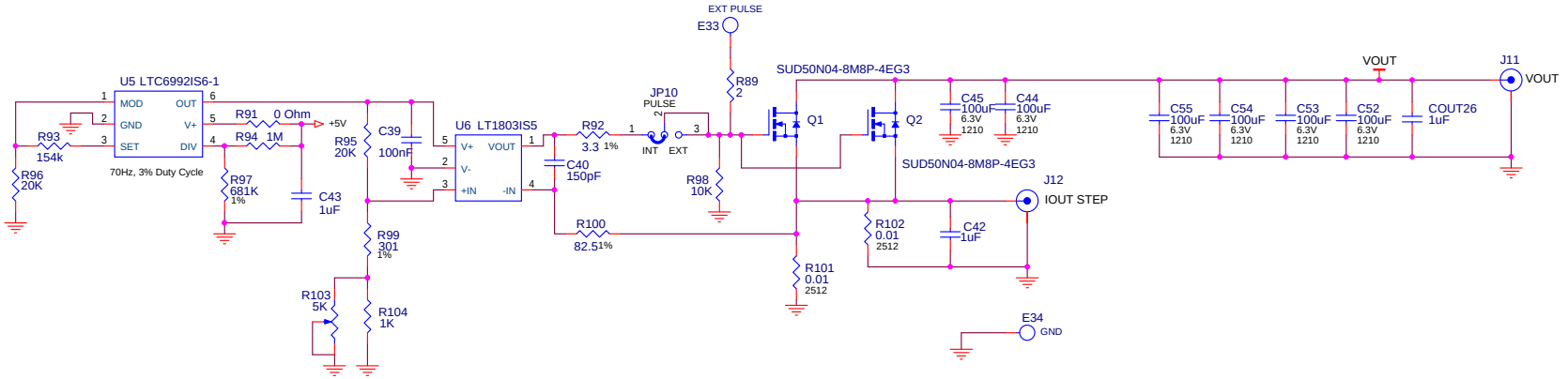
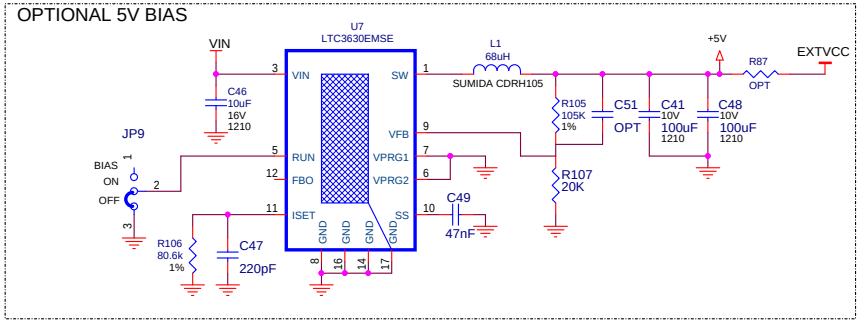
DYNAMIC LOAD CIRCUIT



OPTIONAL 5V BIAS



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JAN I