

V_{WM}=5V, 2pF ESD Protection Diode

FEATURES

Meet IEC61000-4-2(ESD) ±15kV(air) , ±8kV(contact)

Working Voltage: 5V

 Compliant to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC

Halogen-free according to IEC 61249-2-21

APPLICATIONS

• High Speed Data Lines: USB 2.0 / VGA/ DVI /SDI

• Notebooks, Desktops and Servers

Touch Panel

MECHANICAL DATA

Case: DFN1006L

Molding compound meets UL 94 V-0 flammability rating

• Moisture sensitivity level: level 1, per J-STD-020

 Packing code with suffix "G" means green compound (halogen-free)

• Terminal: Matte tin plated leads, solderable per J-STD-002

• Meet JESD 201 class 1A whisker test

Polarity: As marked

• Weight: 0.742 mg (approximately)

KEY PARAMETERS				
PARAMETER	VALUE UNI			
P _{PPSM}	100	W		
I _{PP}	3	Α		
V_{WM}	5	V		
$V_{(BR)}$ at I_R = 1 mA	6	V		
V _C at I _{PP} = 3 A	15	>		
Package	DFN1006L			
Configuration	Single die			











ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER	SYMBOL	TESD5V0L1UC	UNIT		
Marking code on the device		ВН			
Rated random recurring peak Impulse power dissipation (tp=8/20µs waveform)	P _{PPSM}	100	W		
Peak impulse current (tp=8/20µs waveform)	I _{PP}	3	Α		
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	±15 ±8	kV		
Junction temperature range	TJ	-55 to +150	°C		
Storage temperature range	T _{STG}	-55 to +150	°C		





ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Forward voltage per diode (1)	I _R = 1 mA	$V_{(BR)}$	6	-	1	V
Rated working standoff voltage		V_{WM}	-	-	5	V
Reverse current (1)	V _R = 5 V	I _R	-	-	0.1	μA
Clamping voltage (2)	I _{PP} = 1 A	V _C	-	-	10	V
Clamping voltage (2)	I _{PP} = 3 A	V _C	-	-	15	V
Junction capacitance	1 MHz, V _R =0V	Сл	-	-	2	pF

Notes:

- 1. Pulse test with PW=30 ms
- 2. tp=8/20µs waveform

ORDERING INFORMATION				
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING
TESD5V0L1UC (Note 1)	RJ	G	DFN1006L	5K / 7" Reel

Notes:

1. Whole series with green compound

EXAMPLE				
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
TESD5V0L1UC RJG	TESD5V0L1UC	RJ	G	Green compound



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 8/20µs pulse waveform

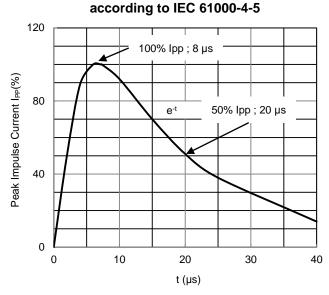


Fig.2 ESD pulse waveform according to IEC 6100-4-2

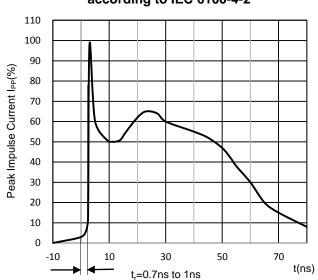


Fig.3 TLP I-V Curve

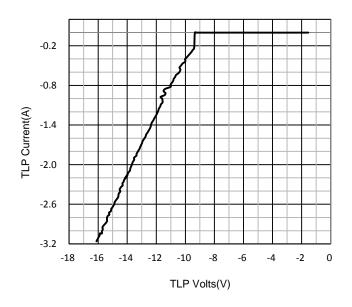
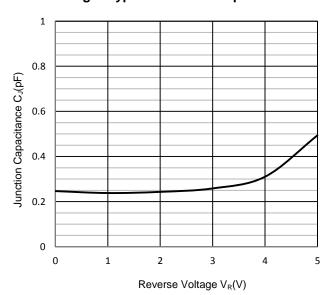


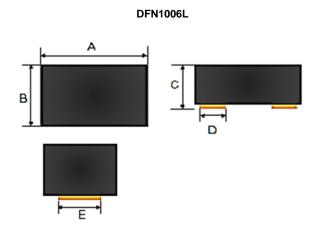
Fig.4 Typical Junction Capacitance





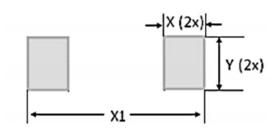


PACKAGE OUTLINE DIMENSION



DIM	Unit (mm)		Unit (inch)	
DIM.	Min	Max	Min	Мах
А	0.950	1.050	0.037	0.041
В	0.550 0.650		0.022	0.026
С	0.360 0.400		0.014	0.015
D	0.300 TYP.		0.012	TYP.
E	0.500 TYP.		0.020	TYP.

SUGGESTED PAD LAYOUT



DIM	Unit (mm)	Unit (inch)	
DIM.	Тур.	Тур.	
Х	0.354	0.014	
X1	1.110	0.044	
Y	0.354	0.014	



Taiwan Semiconductor

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.