

# 1A, 600V Ultra Fast Surface Mount Rectifier

#### **FEATURES**

- Planar technology
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- Lighting application
- Snubber
- Freewheeling application

#### **MECHANICAL DATA**

• Case: SOD-128

Molding compound meets UL 94V-0 flammability rating

• Terminal: Matte tin plated leads, solderable per J-STD-002

• Meet JESD 201 class 2 whisker test

• Polarity: Indicated by cathode band

• Weight: 0.027g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	TINU	
I <sub>F</sub>	1	Α	
$V_{RRM}$	600	V	
I <sub>FSM</sub>	20	Α	
T <sub>J MAX</sub>	150 °C		
Package	SOD-128		
Configuration	Single die		









**SOD-128** 



PARAMETER		SYMBOL	PU1JFS	UNIT
Marking code on the device			PU1JFS	
Repetitive peak reverse voltage		$V_{RRM}$	600	V
Reverse voltage, total rms value		V <sub>R(RMS)</sub>	420	V
Forward current		I <sub>F</sub>	1	А
Surge peak forward current single half sine-wave superimposed on rated load	t = 8.3ms	,	20	
	t = 1.0ms	I <sub>FSM</sub>	50	A
Junction temperature	•	TJ	-55 to +150	°C
Storage temperature		T <sub>STG</sub>	-55 to +150	°C

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THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance	$R_{\Theta JL}$	15	°C/W	
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	74	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	15	°C/W	

**Thermal Performance Note:** Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
	$I_F = 0.5A, T_J = 25^{\circ}C$	V <sub>F</sub>	1.13	-	V
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C		1.25	1.5	V
	I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C		0.90	-	V
	I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C		1.04	-	V
Deverage comment @ reted (/ (2)	T <sub>J</sub> = 25°C	I <sub>R</sub>	-	1	μA
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	T <sub>J</sub> = 125°C		4	-	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	17	-	pF
Daviere a receiver time	$I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A$		-	25	ns
Reverse recovery time	$I_F = 1.0A$ , di/dt = 50A/ $\mu$ s, $V_R = 30V$	- t <sub>rr</sub>	28	-	
Reverse recovery current		I <sub>RM</sub>	1.8	-	Α
Reverse recovery charge	$I_F = 1.0A$ , di/dt = 200A/ $\mu$ s, $V_R = 400V$	Q <sub>rr</sub>	42	-	nC
Reverse recovery time		t <sub>rr</sub>	45	-	ns

# Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
PU1JFS	SOD-128	14,000/ Tape & Reel	



# **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

**Fig.1 Forward Current Derating Curve** 

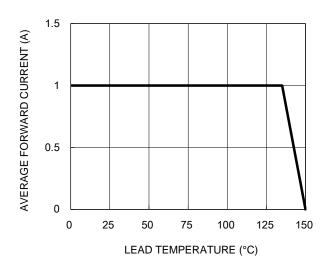
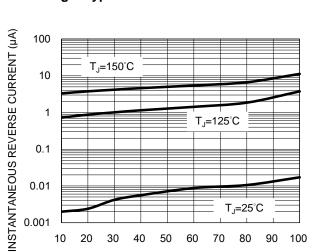


Fig.3 Typical Reverse Characteristics



PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

Fig.2 Typical Junction Capacitance

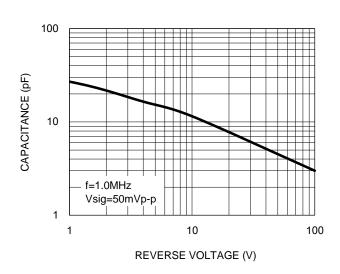


Fig.4 Typical Forward Characteristics

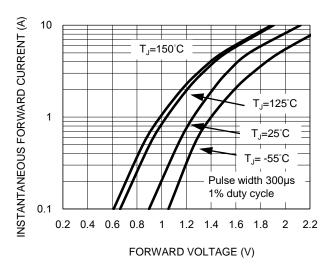
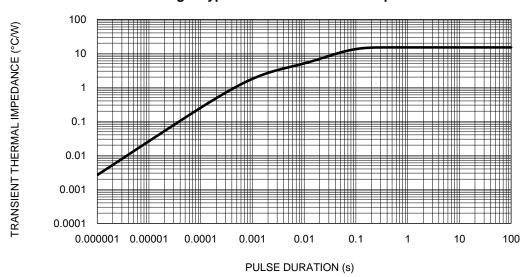


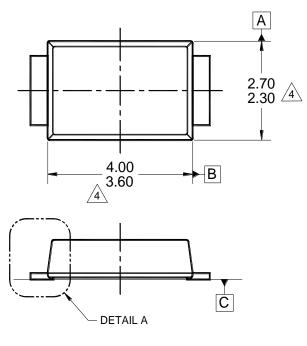
Fig.5 Typical Transient Thermal Impedance

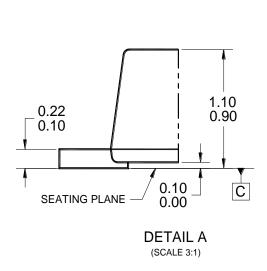


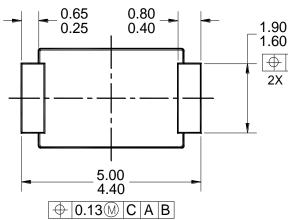


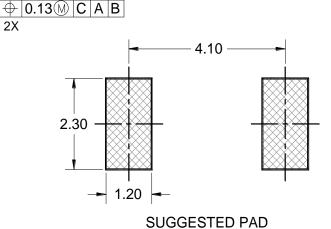
# **PACKAGE OUTLINE DIMENSIONS**

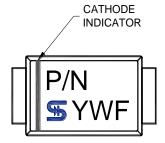
### **SOD-128**











#### NOTES: UNLESS OTHERWISE SPECIFIED

# MARKING DIAGRAM

- P/N = MARKING CODE
- YW = DATE CODE
- F = FACTORY CODE

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.

**LAYOUT** 

- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-221, VARIATION AD, ISSUE B.
- MODED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
- 5. DWG NO. REF: HQ2SD07-SOD128-039 REV A.





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