

Features

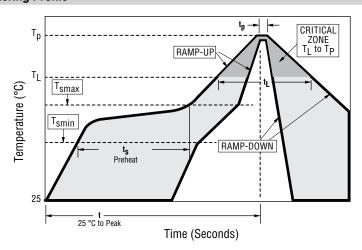
- Wirewound
- Shielded construction
- Metal alloy powder core
- High saturation current
- Low profile
- AEC-Q200 compliant
- RoHS compliant* and halogen free**

SRP2512A Series - Shielded Power Inductors

Electrical Specifications @ 25 °C

Bourns Part Number	Inductance @ 1 MHz /1 V		Q (Typ.) @1 MHz/	SRF (MHz)	DCR (mΩ)	DCR (mΩ)	Irms (A)	Isat (A)
Part Number	L (μΗ)	Tol. (%)	1 V	`Typ.´	Тур.	Max.	Tỳṕ.	(A) Typ.
SRP2512A-R47M	0.47	±20	39	129	21	25	4.6	5.3
SRP2512A-R68M	0.68	±20	39	104	29	35	3.7	5.0
SRP2512A-1R0M	1.0	±20	39	82	41	49	3.5	4.4
SRP2512A-1R5M	1.5	±20	39	71	64	77	2.5	3.2
SRP2512A-2R2M	2.2	±20	39	62	85	98	2.27	3.0
SRP2512A-4R7M	4.7	±20	39	37	196	235	1.55	1.9

Soldering Profile



Profile Feature	Pb Free Assembly			
Average Ramp Rate (T _{Smax} to T _P)	3 °C/second max.			
Preheat - Temperature Min. (T _{Smin}) - Temperature Max. (T _{Smax}) - Time (t _{smin} to t _{smax})	150 °C 200 °C 60-180 seconds			
Time Maintained Above - Temperature (T _L) - Time (t _L)	217 °C 60-150 seconds			
Peak Temperature (Tp)	260 °C +0/-5 °C			
Time within 5 °C of Actual Peak Temperature (Tp)	20-40 seconds			
Ramp-Down Rate	6 °C/second max.			
Time 25 °C to Peak Temperature	8 minutes max.			

- * RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.
- **Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less. Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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General Specifications

Operating Temperature...-40 °C to +125 °C (Temperature rise included) Storage Temperature (Component)

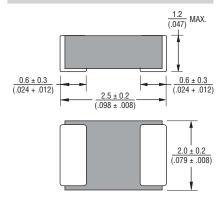
.....50 °C to +125 °C

Rated Current

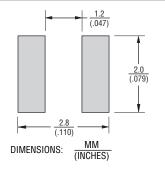
......Inductance drops 30 % at Isat Temperature Rise40 °C at rated Irms Moisture Sensitivity Level (MSL)1 ESD Classification (HBM).......N/A

Materials

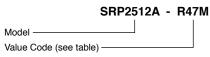
Product Dimensions



Recommended Layout



How to Order

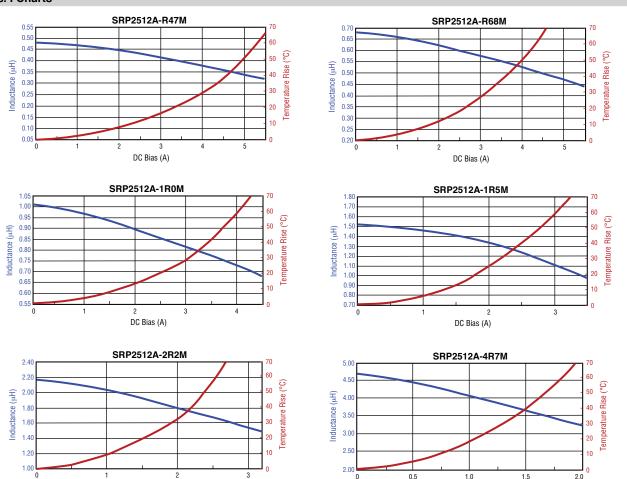


Electrical Schematic



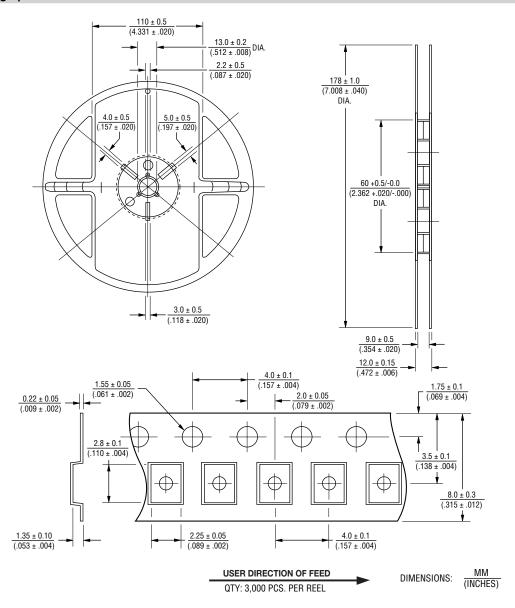
DC Bias (A)

L vs. I Charts



DC Bias (A)

Packaging Specifications



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