



High Speed, Low Voltage, 3 Ω, Quad SPDT CMOS Analog Switch

DESCRIPTION

The DG2706 is a high speed, low voltage, low On-resistance, quad SPDT (single pole double throw) analog switch. It operates from a 1.65 V to 4.3 V single power supply and achieves 3 Ω switch On-resistance. When turned on, each switch conducts equally in both directions. Its switch on resistance flatness is 0.6 Ω and channel to channel matching is of 0.3 Ω when powered with single 3.15 V supply. All channels guaranteed break before make switching.

Control logic input has 0.5 V to 1.65 V logic threshold. It features a 190 MHz - 3 dB bandwidth, - 90 dB crosstalk and - 70 dB off-isolation at 1 MHz.

The DG2706 is an ideal fit for low voltage battery powered devices switching audio, video, multi-media data streams, and control signals between different functional circuits or ports.

The DG2707 comes in a small miniQFN-16 lead package (1.8 mm x 2.6 mm x 0.75 mm). As a committed partner to community and the environment, Vishay Siliconix manufactures this product with the lead(Pb)-free device terminations and is 100 % RoHS compliant.

FEATURES

Operation voltage range: 1.65 V to 4.3 V
Guaranteed On-resistance: 3.0 Ω at 3.15 V



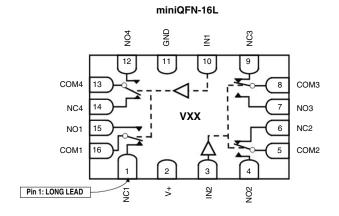
RoHS

- Low voltage logic threshold
- Low crosstalk: 70 dB
- · High off-isolation: 90 dB
- Ultra small package: miniQFN16 of 1.8 mm x 2.6 mm

APPLICATIONS

- · Dual SIM card switch
- A/V and analog signal routing
- · Battery operated devices
- Data acquisition systems
- Communications systems
- · Medical and ATE equipments

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



Top View

Device Marking: VXX Traceability Code: V is DG2706DN XX = Date/Lot

ORDERING INFORMATION					
Temp. Range	Package	Part Number			
- 40 °C to 85 °C	miniQFN-16	DG2706DN-T1-E4			

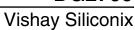


TRUTH TABLE DG2706 QUAD SPDT, miniQFN-16L					
Select Input		On Switches			
IN1 (Pin 10)	IN2 (Pin 3)	Description (Pin)	Common (Pin)		
0	Х	NC1 (Pin 1)	COM1 (Pin 16)		
1	Х	NO1 (Pin 15)	CONT (FIII 16)		
0	Х	NC4 (Pin 14)	COM4 (Bin 10)		
1	Х	NO4 (Pin 12)	COM4 (Pin 13)		
	0	NC2 (Pin 6)	COMO (Din E)		
X	1	NO2 (Pin 4)	COM2 (Pin 5)		
х	0	NC3 (Pin 9)	COM2 (Din 2)		
	1	NO3 (Pin 7)	COM3 (Pin 3)		

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
Parameter		Limit	Unit		
Reference to GND	V+	- 0.3 to 5.0	V		
neleielice to GND	IN, COM, NC, NO ^a	- 0.3 to (V+ + 0.3)	ヿ		
Current (Any terminal except NO, NC or 0	COM)	30			
Continuous Current (NO, NC, or COM)		± 250	mA		
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)		± 500			
Storage Temperature (D Suffix)		- 65 to 150	°C		
Thermal Resistance (Package) ^b	miniQFN-16	152	°C/W		
Power Dissipation (Package) ^b	miniQFN-16 ^{c, d}	525	mW		

Notes:

- a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC board.
- c. Derate 6.6 mW/°C above 70 °C
- d. Manual soldering with iron is not recommended for leadless components. The miniQFN-16 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper lip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.





		Test Conditions		Limits - 40 °C to 85 °C				
Parameter	Symbol	Otherwise Unless Specified	Temp.b	Min. ^d	Typ. ^c	Max. ^d	Unit	
Analog Switch								
Analog Signal Range ^e	V _{ANALOG}	R _{DS(on)}	Full	0		V+	V	
On-Resistance	Р	V: -215 V I -10 mA V -10 V	Room		3	5.5		
On-nesistance	R _{DS(on)}	$V+ = 3.15 \text{ V}, I_{NO/NC} = 10 \text{ mA}, V_{COM} = 1.0 \text{ V}$	Full			6	Ω	
R _{ON} Match	$\Delta R_{(ON)}$	$V+ = 3.15 \text{ V}, I_{NO/NC} = 10 \text{ mA}, V_{COM} = 1.0 \text{ V}$	Room		0.3			
R _{ON} Resistance Flatness	R _{ON}	$V+ = 3.15 \text{ V}, I_{NO/NC} = 10 \text{ mA},$	Room		0.6			
			Room	- 5		5		
Channel Off Leakage	INO/NC(off)	$V+ = 3.6 \text{ V}, V_{NO/NC} = 0.5 \text{ V/3 V},$	Full	- 10		10		
Current	ı	$V_{COM} = 3 \text{ V}/0.5 \text{ V}$	Room	- 5		5	4	
	I _{COM(off)}		Full	- 10		10	nA	
Channel-On Leakage	_	V. 26VV V 2V/05V	Room	- 10		10		
Current	I _{COM(on)}	$V+ = 3.6 \text{ V}, V_{NO/NC}, V_{COM} = 3 \text{ V}/0.5 \text{ V}$	Full	- 20		20		
Digital Control								
Input High Voltage	V_{INH}		Full	1.65			V	
Input Low Voltage	V _{INL}		Full			0.4	V	
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 or V+	Full	- 1		1	μΑ	
Dynamic Characteristics								
Prook Potoro Maka Timo	+		Room		1			
Break-Before-Make Time	t _{BBM}		Full	5			ns	
Enable Turn-On Time	t _{ON(EN)}	V_{NO} , V_{NC} = 1.5 V, R_L = 50 Ω , C_L = 35 pF	Room		20	45		
Enable furn-On Time		V_{NO} , $V_{NC} = 1.5$ V, $H_L = 50.12$, $G_L = 35$ pF	Full			55		
Enable Turn Off Time			Room		15	35		
Enable Turn-Off Time			Full			45		
Charge Injection ^d	Q _{INJ}	$C_L = 1 \text{ nF, } R_{GEN} = 0 \Omega, V_{NC/NO} = 2 \text{ V}$	Room		3		рС	
Off-Isolation ^d	OIRR	V. 215 V f 1 MHz D 50 0 C 5 mE	Room		- 70		٩D	
Crosstalk ^{d, f}	X _{TALK}	$V+ = 3.15 \text{ V}, f = 1 \text{ MHz}, R_L = 50 \Omega, C_L = 5 \text{ pF}$	Room		- 90		dB	
Bandwidth ^d	BW	V+ = 3.15 V, R_L = 50 Ω, C_L = 5 pF, - 3 dB	Room		190		MHz	
Total Harmonic Distortion ^d	THD	$V+ = 3.15 \text{ V}, R_{LOAD} = 600 \Omega$	Room		0.02		%	
N. N. Off Conneither and	CS _{NC(off)}	-			16			
N _O , N _C Off Capacitance ^d	CS _{NO(on)}	V+ = 3.15 V, f = 1 MHz	Room		15		pF	
Channel-On Capacitance ^d	C _{COM(on)}				31			
Power Supply	\-\\-\							
Power Supply Range	V+			1.65		4.3	V	
Power Supply Current I+		V _{IN} = 0 or V+	Full			1	μΑ	

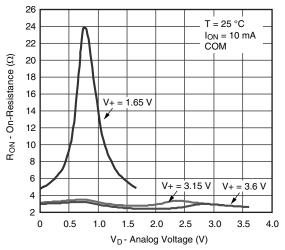
Notes:

- a. Room = $25 \, ^{\circ}$ C, Full = as determined by the operating suffix.
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- c. Typical values are for design aid only, not guaranteed nor subject to production testing.
- d. Guarantee by design, not subjected to production test.
- e. V_{IN} = input voltage to perform proper function.
- f. Crosstalk measured between channels.

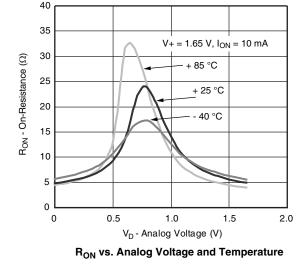
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

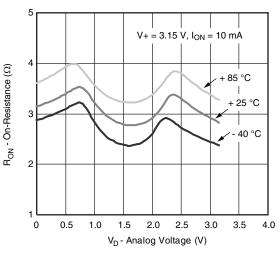
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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

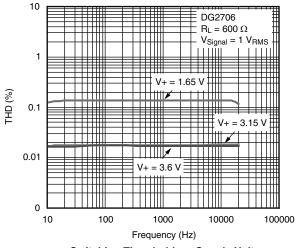


 R_{ON} vs. V_D and Single Supply Voltage

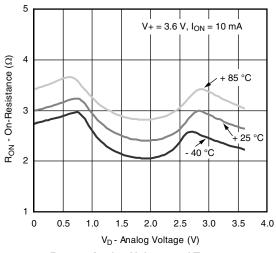




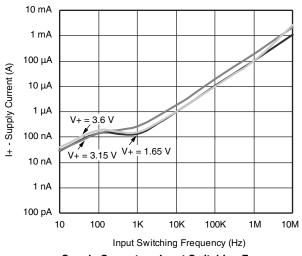
R_{ON} vs. Analog Voltage and Temperature



Switching Threshold vs. Supply Voltage



R_{ON} vs. Analog Voltage and Temperature

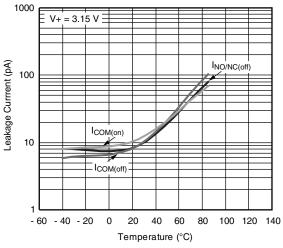


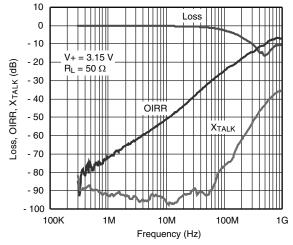
Supply Current vs. Input Switching Frequency





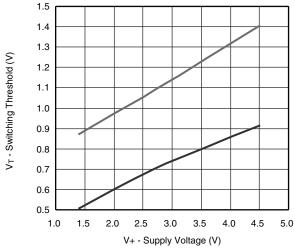
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)





Leakage Current vs. Temperature

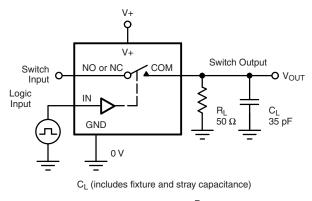
Insertion Loss, Off-Isolation Crosstalk vs. Frequency

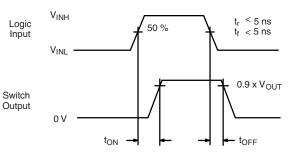


Switching Threshold vs. Supply Voltage

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TEST CIRCUITS





Logic "1" = Switch on Logic input waveforms inverted for switches that have the opposite logic sense.

$$V_{OUT} = V_{COM} \left(\frac{R_L}{R_L + R_{ON}} \right)$$

Figure 1. Switching Time

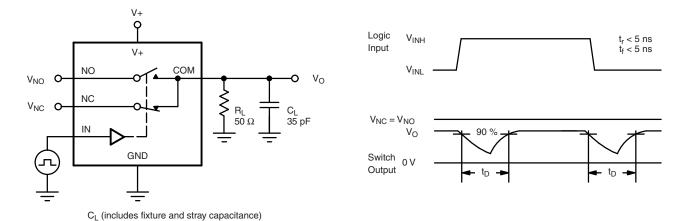


Figure 2. Break-Before-Make Interval

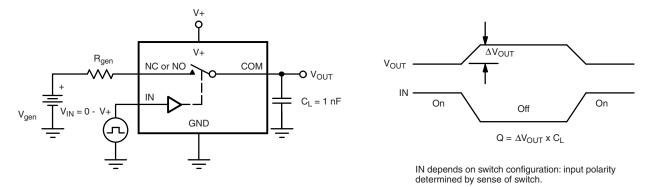


Figure 3. Charge Injection







TEST CIRCUITS

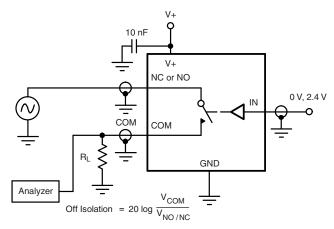


Figure 4. Off-Isolation

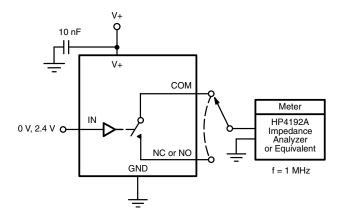
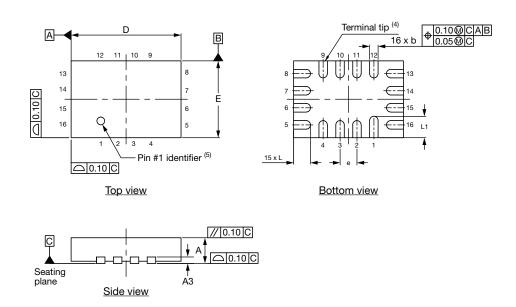


Figure 5. Channel Off/On Capacitance

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Thin miniQFN16 Case Outline



DIMENSIONS	MILLIMETERS (1)			INCHES			
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
А	0.50	0.55	0.60	0.020	0.022	0.024	
A1	0	-	0.05	0	-	0.002	
A3		0.15 ref.			0.006 ref.		
b	0.15	0.20	0.25	0.006	0.008	0.010	
D	2.50	2.60	2.70	0.098	0.102	0.106	
е	0.40 BSC			0.016 BSC			
Е	1.70	1.80	1.90	0.067	0.071	0.075	
L	0.35	0.40	0.45	0.014	0.016	0.018	
L1	0.45	0.50	0.55	0.018	0.020	0.022	
N (3)	16			16			
Nd ⁽³⁾	4			4			
Ne ⁽³⁾	4			4			

Notes

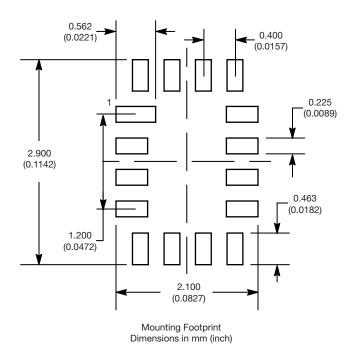
- (1) Use millimeters as the primary measurement.
- (2) Dimensioning and tolerances conform to ASME Y14.5M. 1994.
- (3) N is the number of terminals. Nd and Ne is the number of terminals in each D and E site respectively.
- (4) Dimensions b applies to plated terminal and is measured between 0.15 mm and 0.30 mm from terminal tip.
- (5) The pin 1 identifier must be existed on the top surface of the package by using identification mark or other feature of package body.
- (6) Package warpage max. 0.05 mm.

ECN: T16-0226-Rev. B, 09-May-16

DWG: 6023



RECOMMENDED MINIMUM PADS FOR MINI QFN 16L





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