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U_B12
B12.SchDoc

U_B13
B13.SchDoc

U_B14
B14.SchDoc

U_B15
B15.SchDoc

U_B16
B16.SchDoc

U_B34
B34.SchDoc

U_FPGA-MGT
FPGA-MGT.SchDoc

U_FPGA-CFG
FPGA-CFG.SchDoc

U_FPGA-MISC
FPGA-MISC.SchDoc

U_FPGA-PWR
FPGA-PWR.SchDoc

U_PWR1
PWR1.SchDoc

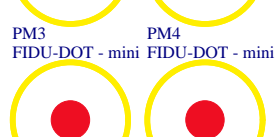
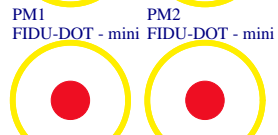
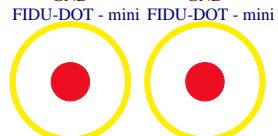
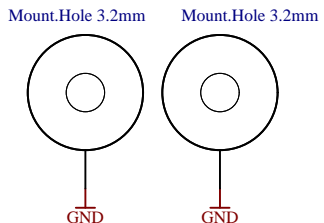
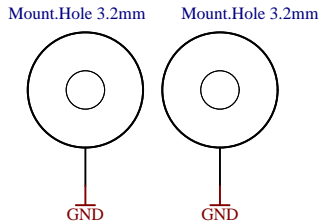
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PWR2.SchDoc

U_Clock
Clock.SchDoc

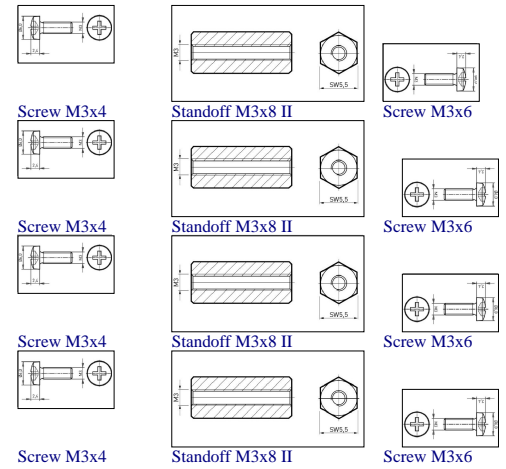
U_B2B-Connectors
B2B-Connectors.SchDoc

U_B33
B33.SchDoc

Serial
Serialnumber 6,3 x 6.3mm



Top of Board



Assembly variant	G2C-1-A
Created by	VT
Modified by	VT
Modified at	2017-01-23
SVN Revision	12616



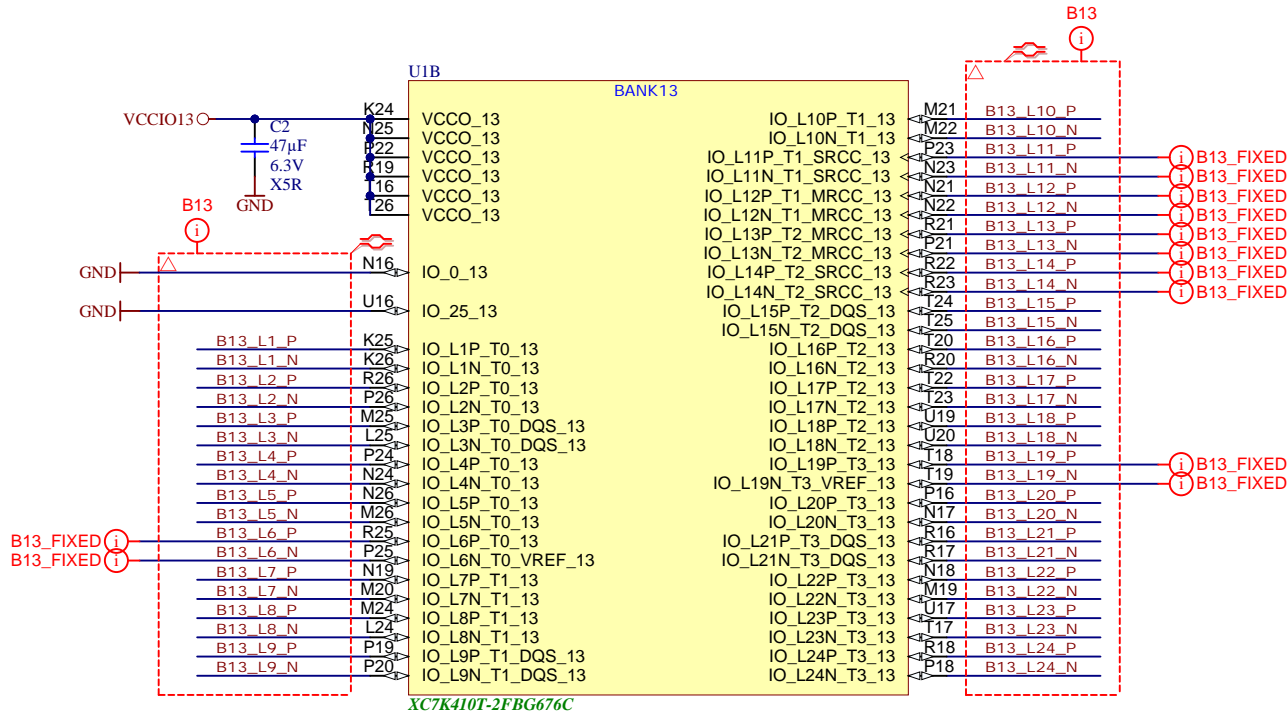
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A4	Number: TE0741 G2C-1-A	Rev. 04
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Filename: TE0741.SchDoc		

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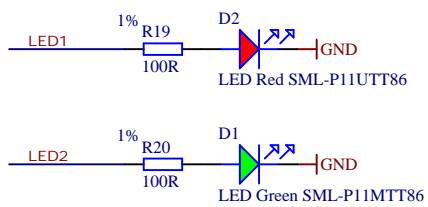
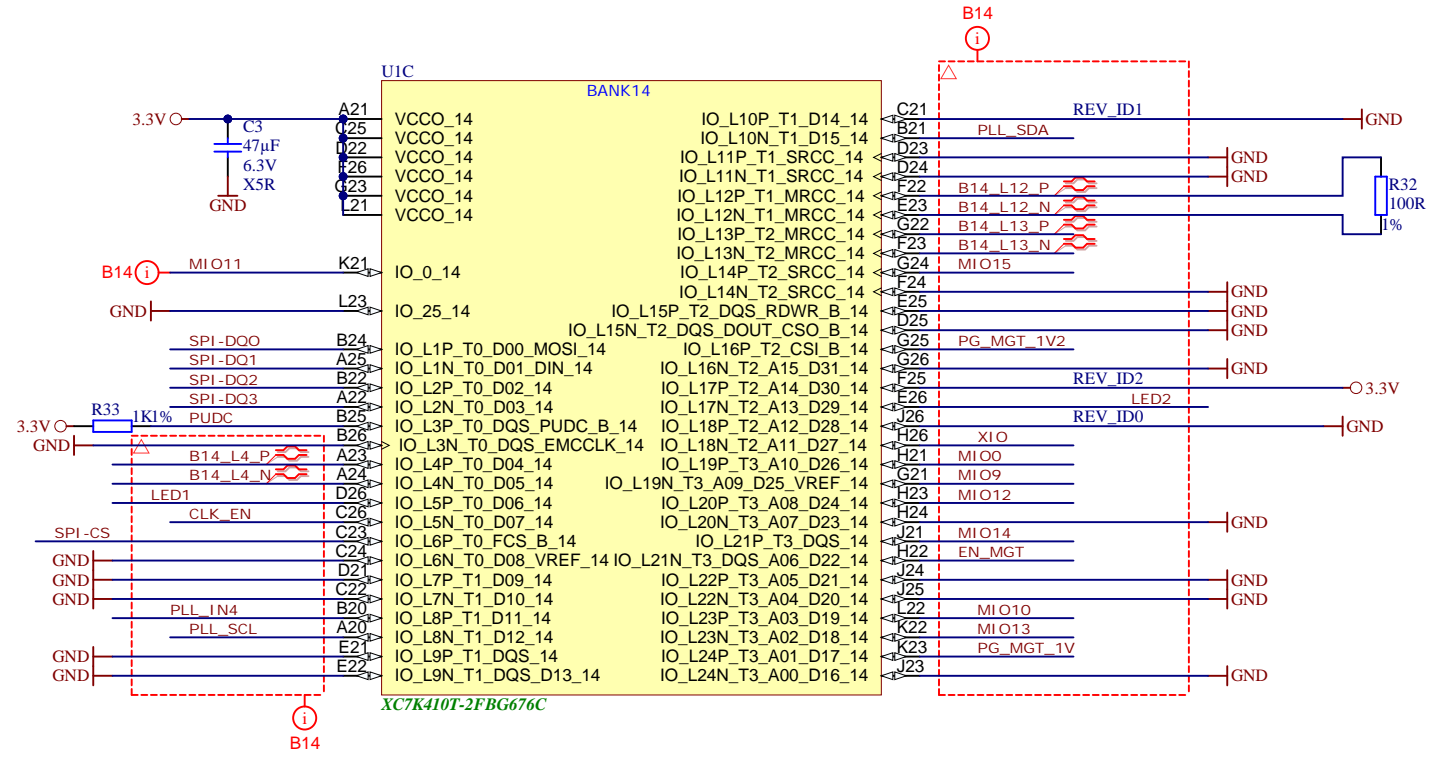
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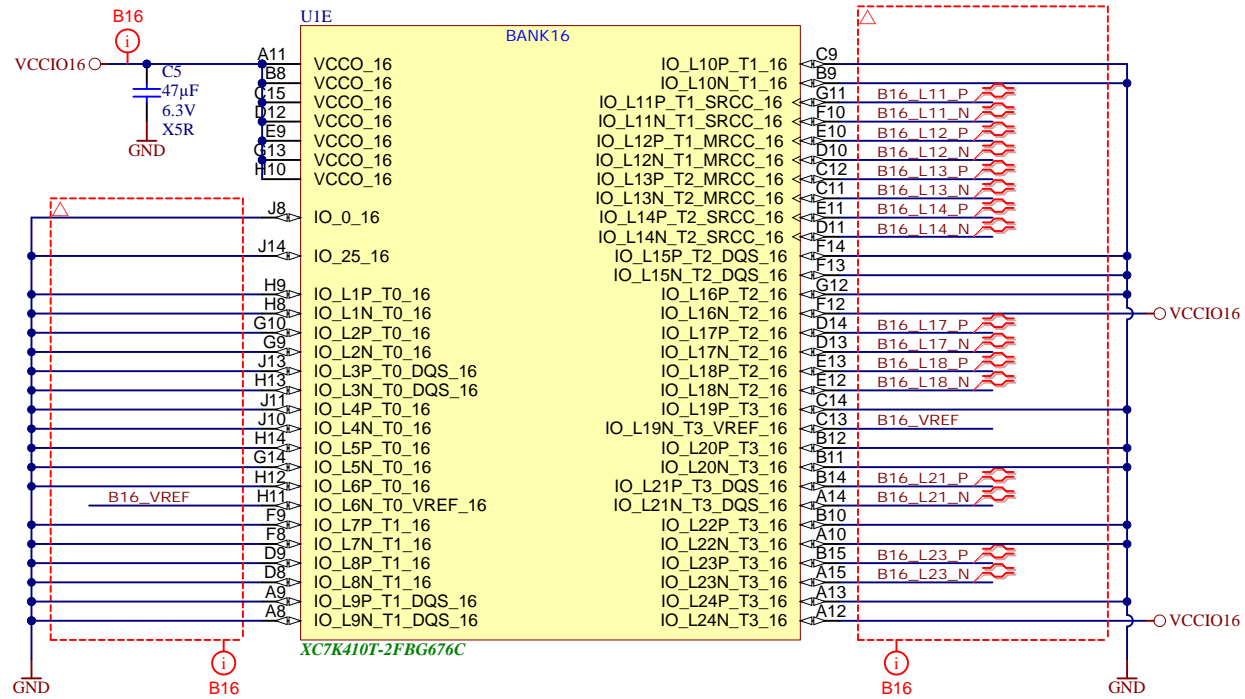
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	Date: 2016-10-25	Copyright: 2013 Trenz Electronic GmbH	Page 4 of 17
	Filename: B13.SchDoc		


Revision PCB Board:

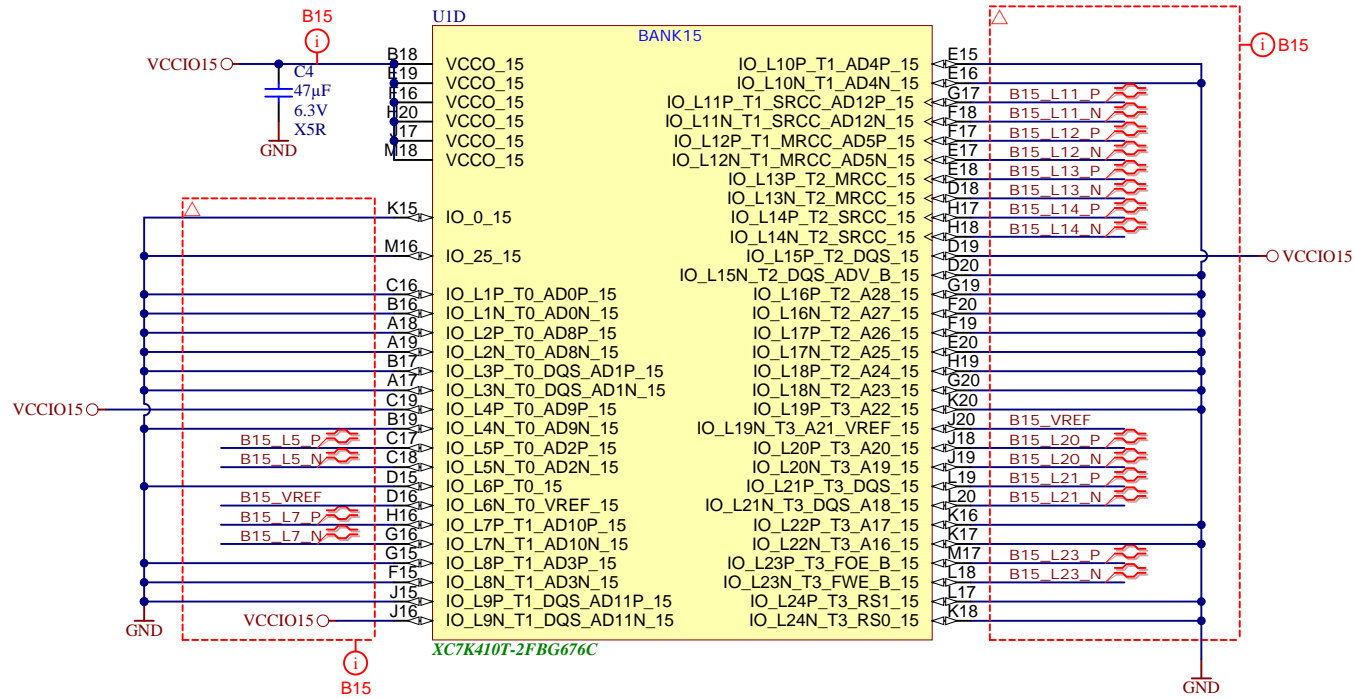
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REV_ID2	0	0	0	0	1



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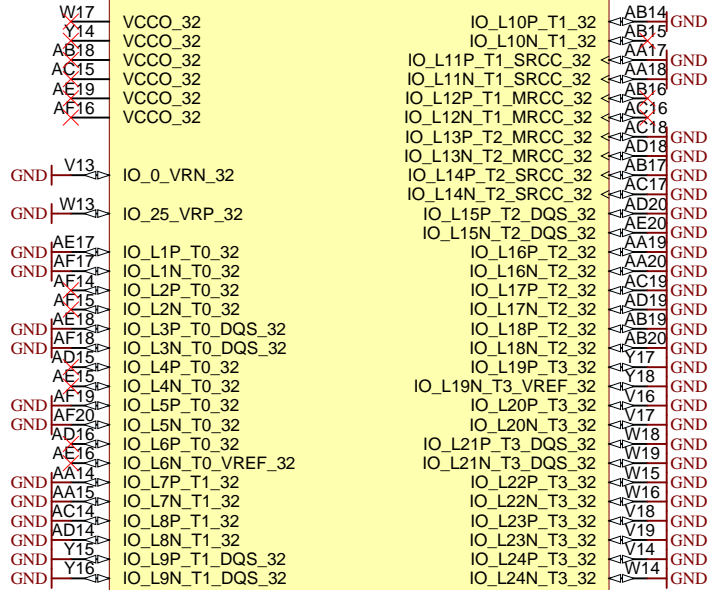
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	Date: 2016-10-25	Copyright: 2013 Trenz Electronic GmbH	Page 6 of 17
	Filename: B16.SchDoc		



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Filename: B15.SchDoc		

UIF

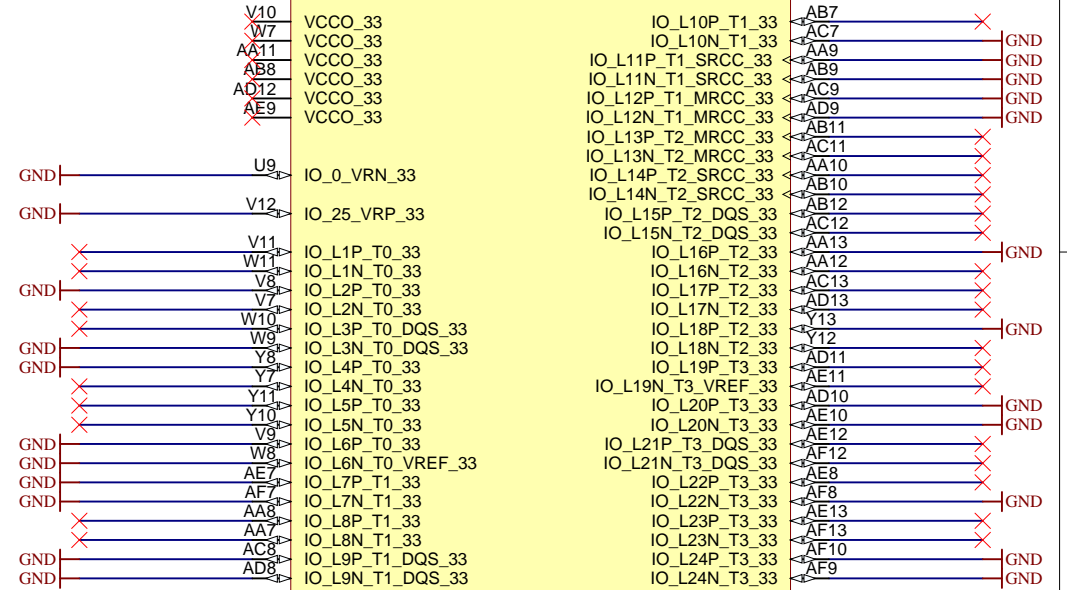
BANK32




XC7K410T-2FBG676C

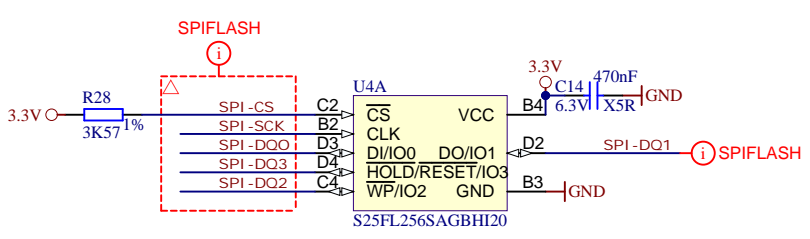
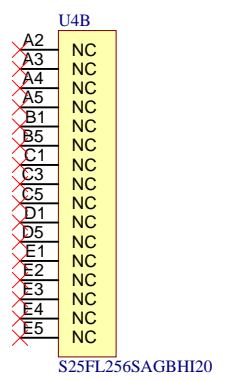
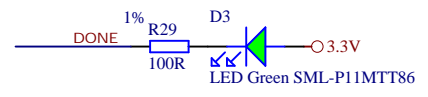
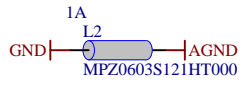
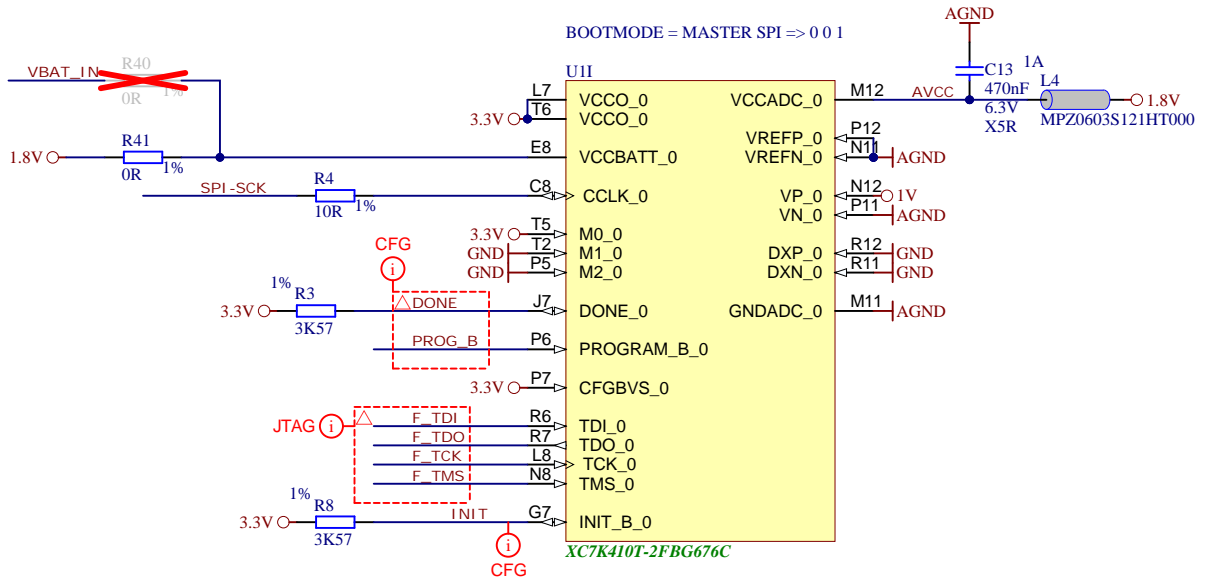
UIG

BANK33

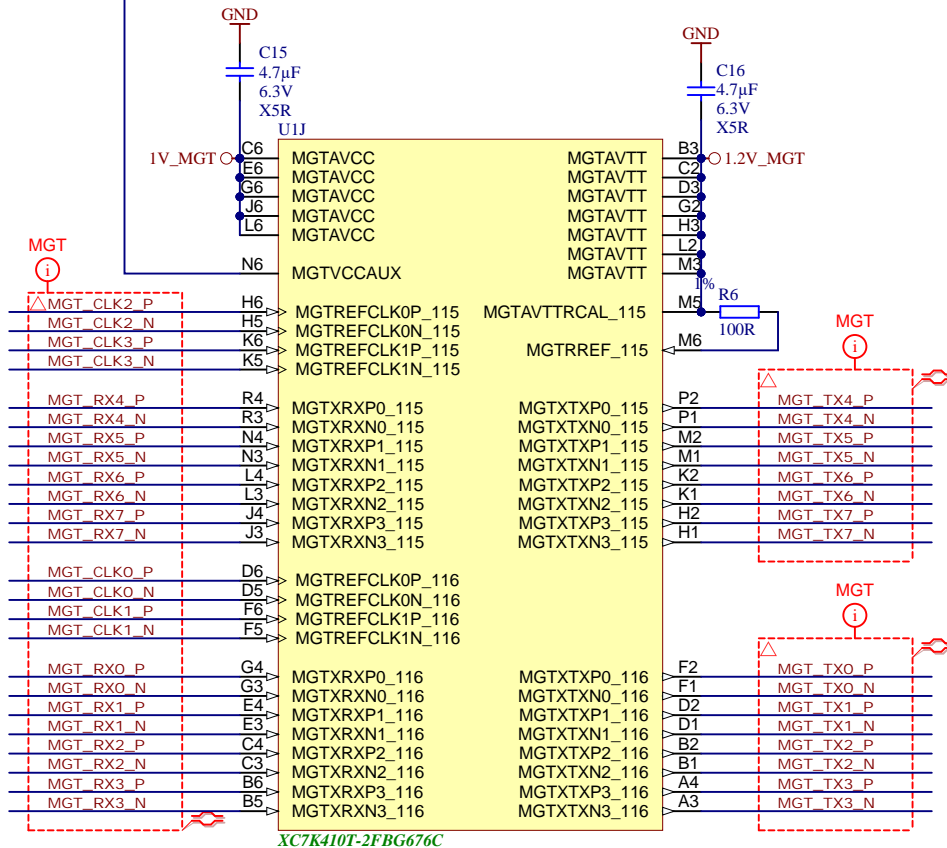
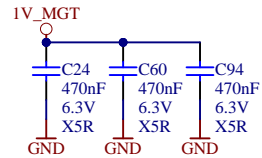
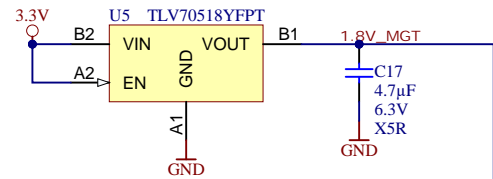


XC7K410T-2FBG676C

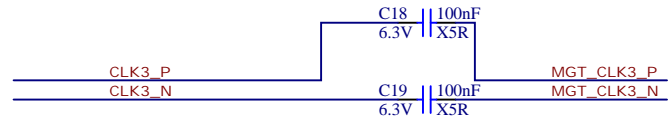
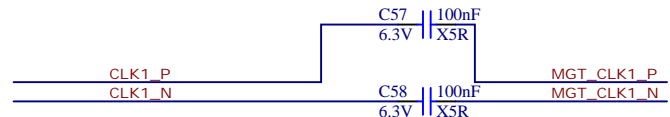
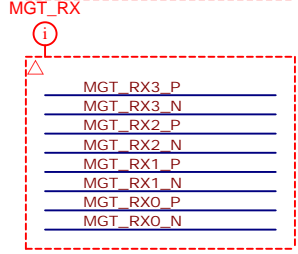
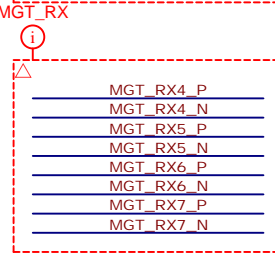
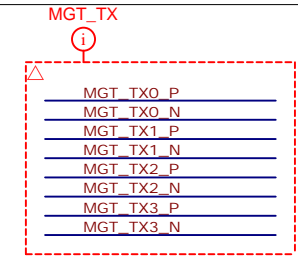
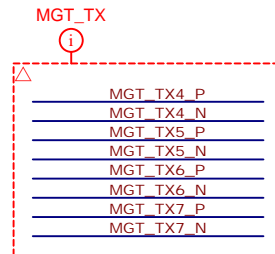
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Date: 2016-10-25		Copyright: 2013 Trenz Electronic GmbH	Rev. 04
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Filename: B33.SchDoc			



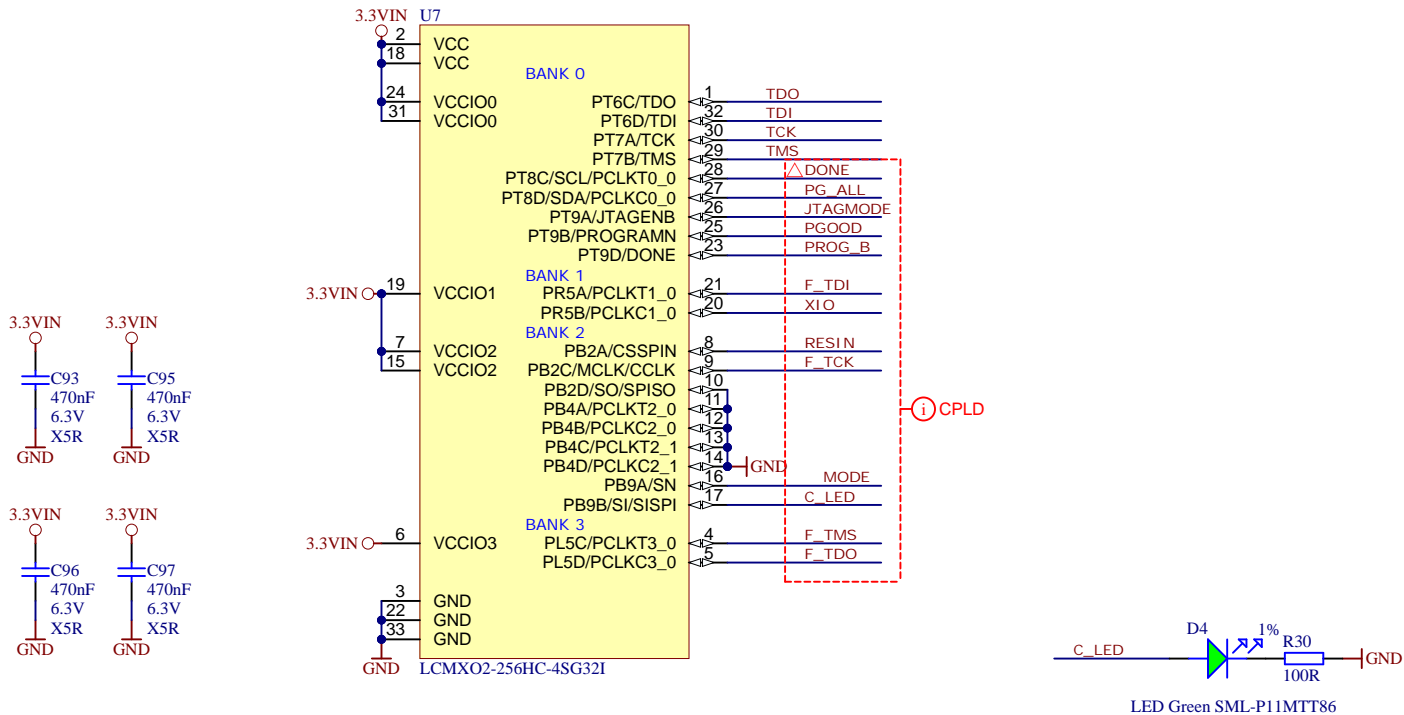
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XC7K410T-2FBG676C



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Filename: FPGA-MGT.SchDoc		



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	Date: 2016-10-25	Copyright: 2013 Trenz Electronic GmbH
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Filename: FPGA-MISC.SchDoc		

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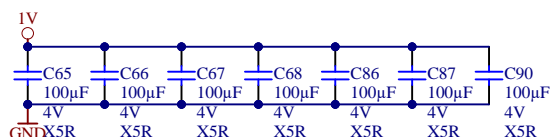
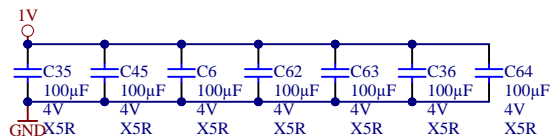
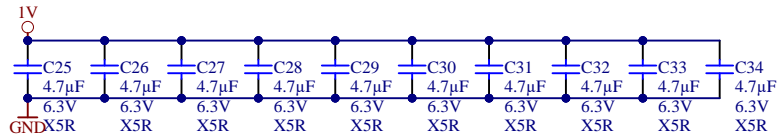
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A

A



B

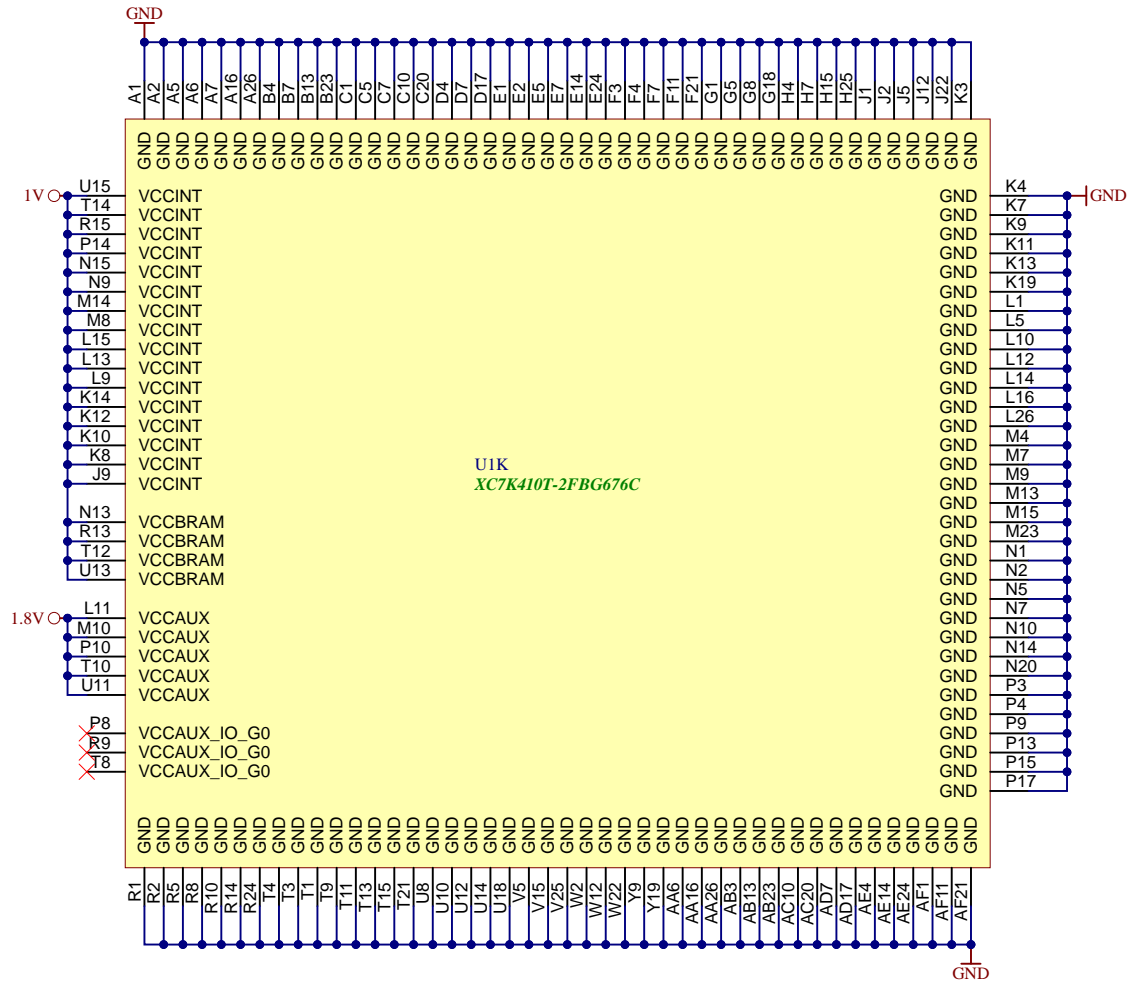
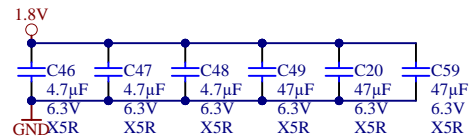
B

C

C

D

D



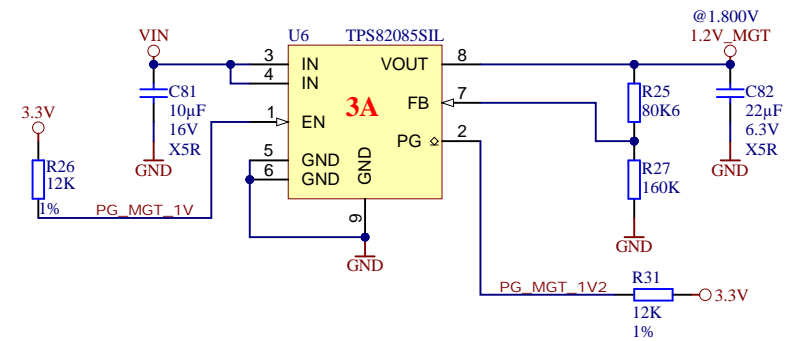
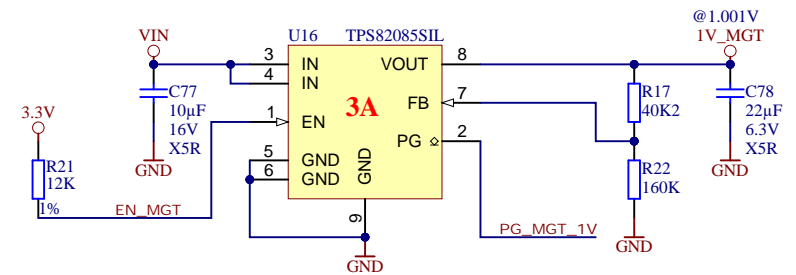
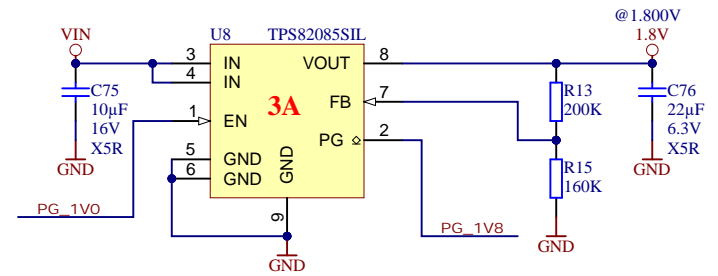
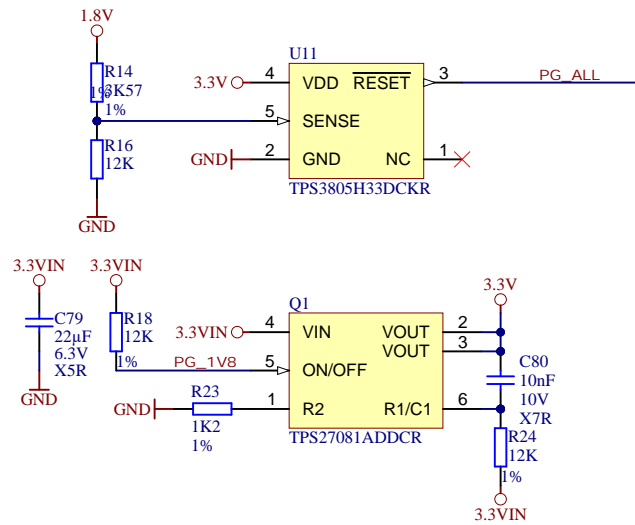
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Date: 2016-10-25	Copyright: 2013 Trenz Electronic GmbH	Page 14 of 17
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CHANGES REV02 to REV03

- 1) fixed connection DCDC for parallel operations
- 2) update Razorbeam Connectors, full update lib
- 3) added serial number (traceability pad)
- 4) changed ferrite beads L1..L4 size 0402 on BKP0603HS121-T
- 5) added thermal vias to mounting holes

Changes REV03 to REV04 (VT:10.2021):

- 1) replaced all DCDC (reason: not recommended use Enpirion DCDC)
- 2) remove Traceability Pad (reason: EOL)
- 3) added additional resistor for VBAT (reason: possibility using external VBAT from Carrier Board)
- 4) full upd LIB components
- 5) optimization PCB routing

A

A

B


B

C

C

D

D

		Title: TE0741 - Changes list	
		A4	Number: TE0741 G2C-1-A
Date: 2016-10-25		Copyright: Trenz Electronic GmbH	
Filename: Revision_Changes.SchDoc		Page 17 of 17	

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