muRata

Chip Monolithic Ceramic Capacitor for Automotive limited to Conductive Glue Mounting GCG188R91E332MA01_ (0603, X8R:EIA, 3300pF, DC25V)

_: packaging code

Reference Sheet

1.Scope

This product specification is applied to Chip Monolithic Ceramic Capacitor limited to Conductive Glue Mounting Type used for Automotive Electronic equipment with conductive glue mounting.

2.MURATA Part NO. System



3. Type & Dimensions



				(Unit:mm)
(1)-1 L	(1)-2 W	(2) T	e	g
1.6±0.2	0.8±0.1	0.8±0.1	0.2 to 0.5	0.5 min.

4.Rated value

	Characteristics ode):X8R(EIA)	(4) Rated	(5) Nominal	(6) Capacitance	Specifications and Test Methods	
Temp. coeff or Cap. Change	1 5		Capacitance	Tolerance	(Operating Temp. Range)	
-15 to 15 %	-55 to 150 °C (25 °C)	DC 25 V	3300 pF	±20 %	-55 to 150 °C	

5.Package

mark	(8) Packaging	Packaging Unit
D	∳180mm Reel PAPER W8P4	4000 pcs./Reel
J	∮330mm Reel PAPER W8P4	10000 pcs./Reel

Product specifications in this catalog are as of Apr.15,2016,and are subject to change or obsolescence without notice. Please consult the approval sheet before ordering.

Please read rating and !Cautions first.



■AEC-Q200 Murata Standard Specification and Test Methods

			Specifi	cation.						
10			Temperature Compensating Type	High Dielectric Type	AEC-Q200 Test Method					
I	Pre-and Post-S Electrical Test	Stress			-					
2	High Temperat		The measured and observed characteristics should satisfy the			Fix the capacitor to the supporting jig in the same manner and				
	Exposure (Storage)		specifications in the following table.			same condi				
		Capacitance	No marking defects Within ±2.5% or ±0.25pF					ours at 150±3°C. Set for		
		Capacitance	(Whichever is larger)	R7/L0/R9.WIUIIII ±12.5%	24±2 110013	s at 100111 te	Inperatur	e, then measure.		
		Q/D.F.	30pFmin. : Q≧1000	R7/L8 : 0.05 max.	4					
		G/D.1.	30pFmax.: Q ≧400+20C	R9 : 0.075max.						
			C: Nominal Capacitance(pF)							
	I.R.		More than 10,000MΩ or 500Ω · F		-					
			R9 : More than 3,000M Ω or 150 $\Omega \cdot$ I	F						
			(Whichever is smaller)							
3 1	Temperature C	ycling	The measured and observed charact	eristics should satisfy the	Fix the cap	acitor to th	e supporti	ng jig in the same manner ar	nd	
			specifications in the following table.		under the s	same condi	tions as N	o.16. Perform the 1000 cycle	es	
		Appearance	No marking defects		according	to the four I	neat treatr	nents listed in the following ta	able.	
		Capacitance	Within ±2.5% or ±0.25pF	R7/L8/R9: Within ±10.0%	Set for 24:	±2 hours a	t room ter	nperature, then measure		
		Change	(Whichever is larger)		Step	1	2	3	4	
		Q/D.F.	30pFmin.: Q ≧1000	R7/L8 W.V.: 25Vmin.: 0.03 max.	Temp. (°C)	-55+0/-3	Room Temp.	125+3/-0(for ∆C/R7) 150+3/-0(for 5G/L8/R9)	Room Temp.	
			30pFmax.: Q ≧ 400+20C	W.V.: 16V : 0.05 max			. onip.		. smp.	
			C: Nominal Capacitance (pF)	R9 : 0.075max.	Time	15±3	1	15±3	1	
		I.R.	More than 10,000ΜΩ or 500Ω •F		(min.)	2_0		.010		
		I.R.	(Whichever is smaller)							
					 Initial me 	asurement	for high d	ielectric constant type		
					Perform a	heat treatm	nent at 150)+0/-10 °C for one hour and t	then set	
					for 24±2 ho	ours at roor	n tempera	iture.		
					Perform th	e initial me	asuremen	t.		
4	Destructive		No defects or abnormalities		Per EIA-46	69.				
	Physical Analys	sis								
5	Moisture Resis	tance	The measured and observed characteristics should satisfy the		Fix the capacitor to the supporting jig in the same manner and					
					Fix the cap	bacilor to th	e supporti	ng jig in the banne manner ar		
			specifications in the following table.		Fix the cap under the s					
			specifications in the following table.		under the s Apply the 2	same condi 24-hour hea	tions as N at (25 to 6	lo.16. 5°C) and humidity (80 to 98%		
			specifications in the following table.		under the s Apply the 2 treatment s	same condi 24-hour hea shown belo	tions as N at (25 to 6 w, 10 con	lo.16. 5°C) and humidity (80 to 98% secutive times.		
		Appearance	No marking defects		under the s Apply the 2 treatment s	same condi 24-hour hea shown belo	tions as N at (25 to 6 w, 10 con	lo.16. 5°C) and humidity (80 to 98%		
		Appearance Capacitance		R7/L8/R9: Within ±12.5%	under the s Apply the 2 treatment s	same condi 24-hour hea shown belo ±2 hours a e	tions as N at (25 to 6 w, 10 con t room ter Hu	io.16. S°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity	5)	
		Capacitance Change	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger)	R7/L8/R9: Within ±12.5%	under the s Apply the 2 treatment s Set for 24= Temperatur (°C)	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter Hu	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	5)	
		Capacitance	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350		under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter ^{Hu} idity ⁸⁰	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
		Capacitance Change	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below:	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24 Temperatur (°C) 70	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter ^{Hu} idity ⁸⁰	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
		Capacitance Change	No marking defects Within $\pm 3.0\%$ or ± 0.30 pF (Whichever is larger) 30 pFmin. : Q ≥ 350 10 pF and over, 30 pF and below: $Q \geq 275+5C/2$	R7/L8/R9: Within ±12.5%	under the s Apply the 2 treatment s Set for 24 = Temperatur (°C) 70 65 60 55 50	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter ^{Hu} idity ⁸⁰	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
		Capacitance Change	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below:	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24 = Temperatur (°C) 70 65 60 55 50 45 40	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter ^{Hu} idity ⁸⁰	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
		Capacitance Change	No marking defects Within $\pm 3.0\%$ or ± 0.30 pF (Whichever is larger) 30 pFmin. : Q ≥ 350 10 pF and over, 30 pF and below: $Q \geq 275+5C/2$	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24 = Temperatur (°C) 70 65 65 60 55 50 45 50 40 35 30	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N at (25 to 6 w, 10 con t room ter ^{Hu} idity ⁸⁰	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
		Capacitance Change	No marking defects Within $\pm 3.0\%$ or ± 0.30 pF (Whichever is larger) 30 pFmin. : Q \geq 350 10 pF and over, 30 pF and below: Q \geq 275+5C/2 10 pFmax.: Q \geq 200+10C	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65 60 65 55 55 50 45 45 40 35	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N tt (25 to 65 w, 10 con t room ter 	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
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		Capacitance Change Q/D.F.	No marking defects Within $\pm 3.0\%$ or ± 0.30 pF (Whichever is larger) 30 pFmin. : Q \ge 350 10 pF and over, 30 pF and below: Q \ge 275+5C/2 10 pFmax.: Q \ge 200+10C C: Nominal Capacitance(pF) More than 10,000MQ or 500Q • F	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max. R9 : 0.075max.	under the s Apply the 2 treatment s Set for 24 = Temperatur (°C) 70 65 60 65 55 50 45 40 35 30 25 20 15 10 5	same condi 24-hour hea shown belo ±2 hours a e Hurr	tions as N tt (25 to 65 w, 10 con t room ter 	6.16. 5°C) and humidity (80 to 98% secutive times. nperature, then measure. midity Humidity ~98% Humidity ⁸⁰ ~98% Hum	b) idity	
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	Biased Humidit	y Appearance Change Q/D.F.	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below: Q≧275+5C/2 10pFmax.: Q≧200+10C C: Nominal Capacitance(pF) More than 10,000MΩ or 500Ω •F R9 : More than 3,000MΩ or 150 Ω • F (Whichever is smaller) The measured and observed charact specifications in the following table. No marking defects Within ±3.0% or ±0.30pF (Whichever is larger)	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max. R9 : 0.075max.	under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65 65 60 65 65 55 50 20 25 20 15 15 15 15 15 10 5 5 5 5 6 0 6 5 5 5 5 5 5 5 5 5 5 5 5	same condi 24-hour hea shown belo ±2 hours a e Hum 90- 10- 10- 10- 10- 10- 10- 10- 10- 10- 1	tions as N at (25 to 6) w, 10 con t room ter wildity 98% +10 +10 -2 c +10 -2 c mt 5 6 7 8 t e supporti tions as N e and 1.3 5% humic 4±2 hours	io.16. 5°C) and humidity (80 to 98% secutive times. mperature, then measure. ^{midity} Humidity 80~98% Hum 90~98% 90~98% 90~ 90~98% 90~ 90~ 90~98% 90~ 90~ 90~ 90~ 90~ 90~ 90~ 90~	idi ty 98%	
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	Biased Humidit	y Appearance Change Q/D.F.	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below: Q≧275+5C/2 10pFmax.: Q≧200+10C C: Nominal Capacitance(pF) More than 10,000MΩ or 500Ω • F R9 : More than 3,000MΩ or 150 Ω • F R9 : More than 3,000MΩ or 150 Ω • F (Whichever is smaller) The measured and observed charact specifications in the following table. No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pF and over: Q≧200 30pF and below: Q≧100+10C/3	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max. R9 : 0.075max.	under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65 65 60 65 55 50 20 25 20 15 15 15 15 15 10 5 5 5 5 0 6 5 5 5 5 5 5 5 5 5 5 5 5 5	same condi 24-hour hea shown belo ±2 hours a e Hum 90- 10- 10- 10- 10- 10- 10- 10- 10- 10- 1	tions as N at (25 to 6) w, 10 con t room ter widity 98% +10 +10 -2 c +10 -2 c mt 5 6 7 8 t e supporti tions as N e and 1.3 5% humic 4±2 hours	io.16. 5°C) and humidity (80 to 98% secutive times. mperature, then measure. ^{midity} Humidity 80~98% Hum 90~98% 90~98% 90~ 90~98% 90~ 90~ 90~98% 90~ 90~ 90~ 90~ 90~ 90~ 90~ 90~	idi ty 98%	
	Biased Humidit	y Appearance Change Q/D.F.	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below: Q≧275+5C/2 10pFmax.: Q ≧200+10C C: Nominal Capacitance(pF) More than 10,000MΩ or 500Ω • F R9 : More than 3,000MΩ or 150 Ω • F (Whichever is smaller) The measured and observed charact specifications in the following table. No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pF and over: Q≧200 30pF and below: Q≧100+10C/3 C: Nominal Capacitance(pF)	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max. R9 : 0.075max. eristics should satisfy the R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65 65 60 65 55 50 20 25 20 15 15 15 15 15 10 5 5 5 5 0 6 5 5 5 5 5 5 5 5 5 5 5 5 5	same condi 24-hour hea shown belo ±2 hours a e Hum 90- 10- 10- 10- 10- 10- 10- 10- 10- 10- 1	tions as N at (25 to 6) w, 10 con t room ter widity 98% +10 +10 -2 c +10 -2 c mt 5 6 7 8 t e supporti tions as N e and 1.3 5% humic 4±2 hours	io.16. 5°C) and humidity (80 to 98% secutive times. mperature, then measure. ^{midity} Humidity 80~98% Hum 90~98% 90~98% 90~ 90~98% 90~ 90~ 90~98% 90~ 90~ 90~ 90~ 90~ 90~ 90~ 90~	idi ty 98%	
	Biased Humidit	y Appearance Change Q/D.F.	No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pFmin. : Q≧350 10pF and over, 30pF and below: Q≧275+5C/2 10pFmax.: Q≧200+10C C: Nominal Capacitance(pF) More than 10,000MΩ or 500Ω • F R9 : More than 3,000MΩ or 150 Ω • F R9 : More than 3,000MΩ or 150 Ω • F (Whichever is smaller) The measured and observed charact specifications in the following table. No marking defects Within ±3.0% or ±0.30pF (Whichever is larger) 30pF and over: Q≧200 30pF and below: Q≧100+10C/3	R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max. R9 : 0.075max. eristics should satisfy the R7/L8/R9: Within ±12.5% R7/L8 : 0.05 max.	under the s Apply the 2 treatment s Set for 24= Temperatur (°C) 70 65 65 60 65 55 50 20 25 20 15 15 15 15 15 10 5 5 5 5 0 6 5 5 5 5 5 5 5 5 5 5 5 5 5	same condi 24-hour hea shown belo ±2 hours a e Hum 90- 10- 10- 10- 10- 10- 10- 10- 10- 10- 1	tions as N at (25 to 6) w, 10 con t room ter widity 98% +10 +10 -2 c +10 -2 c mt 5 6 7 8 t e supporti tions as N e and 1.3 5% humic 4±2 hours	io.16. 5°C) and humidity (80 to 98% secutive times. mperature, then measure. ^{midity} Humidity 80~98% Hum 90~98% 90~98% 90~ 90~98% 90~ 90~ 90~98% 90~ 90~ 90~ 90~ 90~ 90~ 90~ 90~	idi ty 98%	



AEC-Q200 Murata Standard Specification and Test Methods

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٩N	AEC-Q200) Test Item	· · · ·	fication.	AEC-Q200 Test Method		
			Temperature Compensating Type	High Dielectric Type			
7	Operational Life	е	The measured and observed char	acteristics should satisfy the	Fix the capacitor to the supporting jig in the same manner and		
			specifications in the following table		under the same conditions as No.16.		
		Appearance	No marking defects		Apply 200% of the rated voltage for 1000 ± 12 hours at 125 ± 3 °C(for		
		Capacitance	Within ±3.0% or ±0.30pF	R7/L8/R9: Within ±12.5%	Δ C/R7), 150±3°C(for 5G/L8/R9).		
		Change	(Whichever is larger)	-	Set for 24 ± 2 hours at room temperature, then measure.		
		Q/D.F.		R7/L8 : 0.05 max.	The charge/discharge current is less than 50mA.		
			10pF and over, 30pF and below: Q≧275+5C/2	R9 : 0.075max.	 Initial measurement for high dielectric constant type. 		
			0≦275+50/2 10pFmax.: Q ≧200+10C		Apply 200% of the rated DC voltage for one hour at the maximum		
			C: Nominal Capacitance(pF)		operating temperature $\pm 3^{\circ}$ C. Remove and set for 24 ± 2 hours at		
		I.R.	More than 1,000MΩ or 50Ω ·F		room temperature. Perform initial measurement.		
			(Whichever is smaller)				
В	External Visual		No defects or abnormalities		Visual inspection		
9	Physical Dimer	nsion	Within the specified dimensions		Using calipers		
		1					
0	Resistance to	Appearance	No marking defects		Per MIL-STD-202 Method 215		
	Solvents	Capacitance	Within the specified tolerance		Solvent 1 : 1 part (by volume) of isopropyl alcohol		
		Change			3 parts (by volume) of mineral spirits		
		Q/D.F.	30pFmin. : Q≧1000	R7/L8 : W.V.: 25Vmin.: 0.025 max.	Solvent 2 : Terpene defluxer		
			30pFmax.: Q ≧400+20C	W.V.: 16V : 0.035 max.	Solvent 3 : 42 parts (by volume) of water		
			C: Nominal Capacitance(pF)	R9 : 0.075max.	1part (by volume) of propylene glycol monomethyl ether		
					1 part (by volume) of monoethanolamine		
		I.R.					
		1.1X.	More than 10,000M Ω or 500 $\Omega \cdot F$				
			(Whichever is smaller)				
	Shock (Appearance	Within the specified tolerance		Fix the capacitor to the test jig in the same manner and under the		
		Capacitance			same conditions as No.16.Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen		
		Change Q/D.F. 30pFmin. : Q≧1000					
		Q/D.F.		R7/L8 : W.V.: 25Vmin.: 0.025 max.	(18 shocks).		
			30pFmax.: Q ≧400+20C	W.V.: 16V : 0.035 max.	The specified test pulse should be Half-sine and should have a		
			C: Nominal Capacitance(pF)	R9 : 0.075max.	duration :0.5ms, peak value:1500g and velocity change: 4.7m/s.		
		I.R.	More than $10,000M\Omega$ or $500\Omega \cdot F$				
		1.1X.					
			(Whichever is smaller)				
2	Vibration	Appearance	No defects or abnormalities		Fix the capacitor to the test jig in the same manner and under the		
		Capacitance	Within the specified tolerance		same conditions as No.16. The capacitor should be subjected to a		
		Change		Γ	simple harmonic motion having a total amplitude of 1.5mm, the		
		Q/D.F.	30pFmin. : Q≧1000	R7/L8 : W.V.: 25Vmin.: 0.025 max.	frequency being varied uniformly between the approximate limits of		
			30pFmax.: Q ≧400+20C	W.V.: 16V : 0.035 max.	10 and 2000Hz. The frequency range, from 10 to 2000Hz and		
			C: Nominal Capacitance(pF)	R9 : 0.075max.	return to 10Hz, should be traversed in approximately 20 minutes.		
		1.0			This motion should be applied for 12 cycle in each 3 mutually		
		I.R.	More than 10,000M Ω or 500 $\Omega \cdot F$		perpendicular directions		
			(Whichever is smaller)				
3	Thermal Shock	I	The measured and observed char	acteristics should satisfy the	Fix the capacitor to the supporting jig in the same manner and		
5		-	specifications in the following table	•	under the same conditions as No.16. Perform the 300 cycles		
		Appearance	No marking defects		according to the two heat treatments listed in the following		
		Capacitance	Within ±2.5% or ±0.25pF	R7/L8/R9: Within ±10.0%	table(Maximum transfer time is 20 seconds). Set for 24±2 hours at		
		Change	(Whichever is larger)		room temperature, then measure		
		Q/D.F.	30pFmin. : Q≧1000	R7/L8 : W.V.: 25Vmin.: 0.025 max.	Step 1 2		
			30pFmax.: Q ≧400+20C	GCG21BL81H104K: 0.03 max.	125 12/ 0/for (C/P7)		
			C: Nominal Capacitance(pF)	W.V.: 16V : 0.035 max.	Temp.(°C) -55+0/-3 123+3/-0(1012G/R7) 150+3/-0 (for 5G/L8/R9)		
				R9 : 0.075max	Time 15±3 15±3		
			More than 10,000MΩ or 500Ω • F				
		I.R.					
		I.R.	(Whichever is smaller)				
		I.R.			Initial measurement for high dielectric constant type		
		I.R.			Perform a heat treatment at 150+0/-10 °C for one hour and then set		
		I.R.					

■AEC-Q200 Murata Standard Specification and Test Methods

			Specifi	cation.	
No	AEC-Q2	200 Test Item	Temperature Compensating Type	High Dielectric Type	AEC-Q200 Test Method
14	ESD	Appearance Capacitance Change Q/D.F.	No marking defects Within the specified tolerance 30pFmin. : Q≥1000 30pFmax.: Q≥400+20C C: Nominal Capacitance(pF)	R7/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V :0.035 max. R9 : 0.075max.	Per AEC-Q200-002
		I.R.	More than 10,000MΩ or 500Ω • F (Whichever is smaller)	1	
15	Electrical Chatacteri- zation	Appearance Capacitance Change Q/D.F.	No defects or abnormalities Within the specified tolerance 30pFmin. : Q≧1000 30pFmax.: Q≧400+20C C: Nominal Capacitance(pF)	R7/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V : 0.035 max. R9 : 0.075max.	Visual inspection. The capacitance/Q/D.F. should be measured at 25°C at the frequency and voltage shown in the table. Char. $\Delta C,5G$ (more than 1000pF) Item (1000 pF and below) R7,R9,L8(C ≤ 10 μ F) Forgueses: $\Delta C,04H$ In (2014)
		I.R. 25°C	More than 100,000MΩ or 1000Ω+F (Whichever is smaller)	More than 10,000MΩ or 500Ω+F (Whichever is smaller)	$\begin{tabular}{ c c c c c } \hline Frequency & 1\pm0.1MHz & 1\pm0.1kHz \\ \hline Voltage & 0.5 to 5Vrms & 1\pm0.2Vrms \\ \hline \hline \\ \hline $
		I.R. 125℃	More than 10,000MΩ or 100Ω · F (Whichever is smaller)	More than 1,000MΩ or 10Ω+F (Whichever is smaller)	(for 5G/L8/R9) within 2 minutes of charging.
		I.R. 150°C	More than 10,000MΩ or 100Ω+F (Whichever is smaller)	More than 1,000MΩ or 1Ω+F (Whichever is smaller)	
		Dielectric Strength	No failure		No failure should be observed when 250% of the rated voltage is applied between the terminations for 1 to 5 seconds, provided the charge/ discharge current is less than 50mA.
16	Terminal Strength	Appearance	No marking defects		Mount the capacitor on the test jig in Fig.1 using a conductive glue (HEREAUS"PC3000").
		Capacitance Change Q/D.F.	Within specified tolerance 30pFmin. : Q≧1000 30pFmax.: Q≧400+20C	R7/L8 : W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035max.	The conductive glue is hardened at 140°C for 30minites. Then apply *shear tension in parallel with the test jig for 60sec. *Show in the table 1
		I.R.	C: Nominal Capacitance(pF) More than 10,000MΩ or 500Ω · F (Whichever is smaller) Type Shar GCG15 GCG18 GCG21 GCG31	e Tension 2. 0N 2. 7N 4. 9N 6. 9N 12. 6N	Snow in the table 1 Ag Pd electrode Alumina Image: transmission of the table 1 Type a Image: transmission of table 1 Type a Image: transmission of table 1 Image: transmissinterm of table 1 Image: transmi

AEC-Q200 Murata Standard Specification and Test Methods

-0.11

0.25

				Specificati	ion.		
No	AEC-Q200) Test Item	Temperatur Compensating	e	High Dielectric T	уре	AEC-Q200 Test Method
17	Beam Load Test		Chip < Chip L dimension : 3 Chip	5mm max. > thickness > 0.5r thickness ≤ 0.5	mm rank : 20N imm rank : 8N 5mm rank : 15N		Place the capacitor in the beam load fixture as Fig 2. Apply a force. < Chip Length : 2.5mm max. >
8	Capacitance Temperature Characteristics	Capacitance Change	Within the specified tol (Table A)	L8 :	: Within $\pm 15\%$ (-55°C to +125 Within $\pm 15\%$ (-55°C to +125° Within +15/-40% (+125°C to +150° : Within $\pm 15\%$ (-55°C to +150°	(3° (3°(The capacitance change should be measured after 5 min. at each specified temperature stage. (1)Temperature Compensating Type The temperature coefficient is determined using the capacitance measured in step 3 as a reference. When cycling the temperature sequentially from step1 through 5 (Δ C: +25°C to +125°C, 5G:+25°C to +150°C other temp. coifficient.:+25°C to +85°C) the capacitance should be within the specified tolerance for the temperature coefficient and capacitance change as Table A-1. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step
		Temperature Coefficient	Within the specified tol (Table A)	erance.			1,3 and 5 by the cap value in step 3. Step Temperature.(°C) 1 25±2 2 -55±3(for ΔC to R7) 3 25±2
		Capacitance Drift	Within ±0.2% or ±0.0 (Whichever is larger.)	15 pF			4 125±3(for ∆C/R7), 150±3(for 5G/R9/L8) 5 25±2 (2) High Dielectric Constant Type The ranges of capacitance change compared with the above 25°C value over the temperature ranges shown in the table should be within the specified ranges. Initial measurement for high dielectric constant type. Perform a heat treatment at 150+0/-10°C for one hour and then set for 24±2 hours at room temperature. Perform the initial measurement.
-	Table A	1	I	V			
ſ		Nominal Valu			acitance Chan	ge from 2	25°C (%)
	Char.	(ppm/°C)		55 Min.	-30 Max.	0 Min.	-10 Max. Min.
			Max.	iviin.	iviax.	iviin.	IVIAX. IVIIII.

-0.24 Note 1: Nominal values denote the temperature coefficient within a range of 25°C to 125°C(for ∆C)/ 150°C(for 5G).

0.58

 0 ± 30

5C/5G

0.40

-0.17



1.Tape Carrier Packaging(Packaging Code:D/E/W/F/L/J/K)

1.1 Minimum Quantity(pcs./reel)

			φ180mm reel	φ330mm reel		
Туре		Paper	[.] Tape	Plastic Tape	Paper Tape	Plastic Tape
		Code:D/E	Code:W	Code:L	Code:J/ F	Code:K
GCG15	5	10000	20000	/	50000	/
60615	5	(W8P2)	(W8P1)		(W8P2)	
GCG18	8	4000			10000	
	6	4000			10000	
GCG21	9	4000			10000	
	В			3000		10000
GCG31	М			3000		10000
60631	С			2000		6000
GCG32	D			1000		4000
60632	E			1000		4000

1.2 Dimensions of Tape

(1)GCG15(W8P2 CODE:D/E/J/F) <Paper Tape>



(2)GCG15(W8P1 CODE:W) <Paper Tape> 1.75±0.1 1.0±0.05 ϕ 1. 5 $^{+0.1}_{-0}$ \leftrightarrow 3.5±0.05 8.0±0.3 -+--÷ \leftrightarrow 1.0 ± 0.05 1_tſ Dimensions A *3 B *3 Туре t W L GCG15 ±0.05 0.65 1.15 0.8 max. 5 ±0.1 *3 Nominal value

(in:mm)

(in:mm)

Package GCG Type

(3)GCG18/21 <Paper Tape>



Туре		Dimensions Tolerance(Chip)		TDimensions	А	В	t
.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		L	W	(Chip)		_	
GCG18	8	±0.2	±0.1	0.8±0.1	1.05±0.10	1.85±0.10	
GCG21	6	+0.2	+0.0	0.6±0.1	1 55 ± 0.45	2 20 ± 0 45	1.1max.
60621	9	±0.3	±0.2	0.85±0.1	1.55±0.15	2.30±0.15	

(4)GCG21/31/32 <Plastic Tape>

D

Е

 ± 0.4

 ± 0.3

GCG32



 2.0 ± 0.3

 2.5 ± 0.3

 2.80 ± 0.20

 3.50 ± 0.20

3.0 max.

3.7 max.

(in:mm)





- 1.3 Tapes for capacitors are wound clockwise shown in Fig.3.
 - (The sprocket holes are to the right as the tape is pulled toward the user.)
- 1.4 Part of the leader and part of the vacant section are attached as follows.



- 1.5 Accumulate pitch : 10 of sprocket holes pitch = 40±0.3mm
- 1.6 Chip in the tape is enclosed by top tape and bottom tape as shown in Fig.1.
- 1.7 The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.
- 1.8 There are no jointing for top tape and bottom tape.
- 1.9 There are no fuzz in the cavity.
- 1.10 Break down force of top tape : 5N min. Break down force of bottom tape : 5N min. (Only a bottom tape existence)
- 1.11 Reel is made by resin and appeaser and dimension is shown in Fig 2. There are possibly to change the material and dimension due to some impairment.
- 1.12 Peeling off force : 0.1N to 0.6N in the direction as shown below.



1.13 Label that show the customer parts number, our parts number, our company name, inspection number and quantity, will be put in outside of reel.

Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

①Aircraft equipment
 ②Aerospace equipment
 ③Undersea equipment
 ④Power plant control equipment
 ⑤Medical equipment
 ⑥Transportation equipment(vehicles,trains,ships,etc.)
 ⑦Traffic signal equipment
 ⑧Data-processing equipment
 ⑩Application of similar complexity and/or reliability requirements to the applications listed in the above.

Storage and Operation condition

- 1. If store the chip monolithic ceramic capacitors in an atmosphere consisting of high temperature or humidity, sulfur or chlorine gases, contaminants attach to the surface of external electrode, and bondability with conductive glue may deteriorate. Do not store the capacitors in an atmosphere consisting of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammoria gas, etc.). Storage environment must be at room temperature of +5°C to +40°C and a relative humidity of 20% to 70%, and use the product within six months. In case of packaging, do not open the last wrappend, polyethylene bag, till just before using. After unpacking, immediately reseal, or store in a desiccator containing a desiccant.
- 2. Due to moisture condensation caused by rapid humidity changes, or the photochemical change caused by direct sunlight on the terminal electrodes and/or the resin/epoxy coatings, the bondability with conductive glue and electrical performance may deteriorate. Do not store capacitors under direct sunlight or in high humidity conditions.
- 3. This product is chip monolithic ceramic capacitor limited to conductive glue mounting. Do not apply mounting method other than conductive glue. Flow or reflow soldering can result in a lack of adhesive strength on the outer electrode by poor wettability, which may result in chips breaking loose from the PCB.

Rating

1.Temperature Dependent Characteristics

- 1. The electrical characteristics of the capacitor can change with temperature.
- 1-1. For capacitors having larger temperature dependency, the capacitance may change with temperature changes. The following actions are recommended in order to ensure suitable capacitance values.
- (1) Select a suitable capacitance for the operating temperature range.
- (2) The capacitance may change within the rated temperature. When you use a high dielectric constant type capacitor in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the temperature characteristics, and carefully confirm the various characteristics in actual use conditions and the actual system.



[Example of Temperature Caracteristics X7R(R7)]

Sample: 0.1µF, Rated Voltage 50VDC





2.Measurement of Capacitance

- 1. Measure capacitance with the voltage and frequency specified in the product specifications.
- 1-1. The output voltage of the measuring equipment may decrease occasionally when capacitance is high. Please confirm whether a prescribed measured voltage is impressed to the capacitor.
- 1-2. The capacitance values of high dielectric constant type capacitors change depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.

3.Applied Voltage

- 1. Do not apply a voltage to the capacitor that exceeds the rated voltage as called out in the specifications.
- 1-1. Applied voltage between the terminals of a capacitor shall be less than or equal to the rated voltage.
- (1) When AC voltage is superimposed on DC voltage, the zero-to-peak voltage shall not exceed the rated DC voltage. When AC voltage or pulse voltage is applied, the peak-to-peak voltage shall not exceed the rated DC voltage.
- (2) Abnormal voltages (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated DC voltage.



(E : Maximum possible applied voltage.)

1-2. Influence of over voltage

Over voltage that is applied to the capacitor may result in an electrical short circuit caused by the breakdown of the internal dielectric layers .

The time duration until breakdown depends on the applied voltage and the ambient temperature.

4.Type of Applied Voltage and Self-heating Temperature

1.Confirm the operating conditions to make sure that no large current is flowing into the capacitor due to the continuous application of an AC voltage or pulse voltage.

When a DC rated voltage product is used in an AC voltage circuit or a pulse voltage circuit, the AC current or pulse current will flow into the capacitor; therefore check the self-heating condition.

Please confirm the surface temperature of the capacitor so that the temperature remains within the upper limits of the operating temperature, including the rise in temperature due to self-heating. When the capacitor is used with a high-frequency voltage or pulse voltage, heat may be generated by dielectric loss.

<Applicable to Rated Voltage of less than 100VDC>

 1-1. The load should be contained to the level such that when measuring at atmospheric temperature of 25°C, the product's self-heating remains below 20°C and the surface temperature of the capacitor in the actual circuit remains within the maximum operating temperature. $\label{eq:constraint} \begin{array}{l} [Example of Temperature Rise (Heat Generation) in Chip \\ Monolithic Ceramic Capacitors in Contrast to Ripple Current] \\ Sample: R(R1) characteristics 10 \mu F, Rated voltage: DC10V \end{array}$



muRata ∆Caution

5. DC Voltage and AC Voltage Characteristic

- The capacitance value of a high dielectric constant type capacitor changes depending on the DC voltage applied. Please consider the DC voltage characteristics when a capacitor is selected for use in a DC circuit.
- 1-1. The capacitance of ceramic capacitors may change sharply depending on the applied voltage. (See figure) Please confirm the following in order to secure the capacitance.
- (1) Determine whether the capacitance change caused by the applied voltage is within the allowed range .
- (2) In the DC voltage characteristics, the rate of capacitance change becomes larger as voltage increases, even if the applied voltage is below the rated voltage. When a high dielectric constant type capacitor is used in a circuit that requires a tight (narrow) capacitance tolerance (e.g., a time constant circuit), please carefully consider the voltage characteristics, and confirm the various characteristics in the actual operating conditions of the system.
- The capacitance values of high dielectric constant type capacitors changes depending on the AC voltage applied.
 Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.

6. Capacitance Aging

 The high dielectric constant type capacitors have an Aging characteristic in which the capacitance value decreases with the passage of time. When you use a high dielectric constant type capacitors in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics. In addition, check capacitors using your actual appliances at the intended environment and operating conditions.

7.Vibration and Shock

- 1. Please confirm the kind of vibration and/or shock, its condition, and any generation of resonance. Please mount the capacitor so as not to generate resonance, and do not allow any impact on the terminals.
- Mechanical shock due to being dropped may cause damage or a crack in the dielectric material of the capacitor. Do not use a dropped capacitor because the quality and reliability may be deteriorated.
- 3. When printed circuit boards are piled up or handled, the corner of another printed circuit board should not be allowed to hit the capacitor in order to avoid a crack or other damage to the capacitor.







[Example of Change Over Time (Aging characteristics)]





Mounting

1. Selection of Conductive Adhesive, Mounting Process, and Bonding Strength

1. The acuired bonding strength may change greatly depending on the conductive adhesive to be used. Be sure to confirming the desired performance can be acquired in the assumed monting process with the conductive adhesive to be used.

2.Maintenance of the Mounting (pick and place) Machine

- 1. Make sure that the following excessive forces are not applied to the capacitors.
- 1-1. In mounting the capacitors on the printed circuit board, any bending force against them shall be kept to a minimum to prevent them from any damage or cracking. Please take into account the following precautions and recommendations for use in your process.
 - (1) Adjust the lowest position of the pickup nozzle so as not to bend the printed circuit board.
 - (2) Adjust the nozzle pressure within a static load of 1N to 3N during mounting.



2.Dirt particles and dust accumulated between the suction nozzle and the cylinder inner wall prevent the nozzle from moving smoothly. This imposes greater force upon the chip during mounting, causing cracked chips. Also, the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked and replaced periodically.

3.Moisture proof

1. To prevent the silver electrode migration, keep parts under low moisture condition with resin coating and the equivalent.

4.Coating

 A crack may be caused in the capacitor due to the stress of the thermal contraction of the resin during curing process. The stress is affected by the amount of resin and curing contraction. Select a resin with low curing contraction. The difference in the thermal expansion coefficient between a coating resin or a molding resin and the capacitor may cause the destruction and deterioration of the capacitor such as a crack or peeling, and lead to the deterioration of insulation resistance or dielectric breakdown.

Select a resin for which the thermal expansion coefficient is as close to that of the capacitor as possible. A silicone resin can be used as an under-coating to buffer against the stress.

- Select a resin that is less hygroscopic. Using hygroscopic resins under high humidity conditions may cause the deterioration of the insulation resistance of a capacitor. An epoxy resin can be used as a less hygroscopic resin.
- 3. The halogen system substance and organic acid are included in coating material, and a chip corrodes by the kind of Coating material. Do not use strong acid type.

Others

1. Under Operation of Equipment

- 1-1. Do not touch a capacitor directly with bare hands during operation in order to avoid the danger of an electric shock.
- 1-2. Do not allow the terminals of a capacitor to come in contact with any conductive objects (short-circuit). Do not expose a capacitor to a conductive liquid, inducing any acid or alkali solutions.
- 1-3. Confirm the environment in which the equipment will operate is under the specified conditions.
 - Do not use the equipment under the following environments.
 - (1) Being spattered with water or oil.
 - (2) Being exposed to direct sunlight.
 - (3) Being exposed to ozone, ultraviolet rays, or radiation.
 - (4) Being exposed to toxic gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
 - (5) Any vibrations or mechanical shocks exceeding the specified limits.
 - (6) Moisture condensing environments.
- 1-4. Use damp proof countermeasures if using under any conditions that can cause condensation.

2. Others

- 2-1. In an Emergency
- (1) If the equipment should generate smoke, fire, or smell, immediately turn off or unplug the equipment. If the equipment is not turned off or unplugged, the hazards may be worsened by supplying continuous power.
- (2) In this type of situation, do not allow face and hands to come in contact with the capacitor or burns may be caused by the capacitor's high temperature.
- 2-2. Disposal of waste

When capacitors are disposed of, they must be burned or buried by an industrial waste vendor with the appropriate licenses.

- 2-3. Circuit Design
- (1) Addition of Fail Safe Function

Capacitors that are cracked by dropping or bending of the board may cause deterioration of the insulation resistance, and result in a short. If the circuit being used may cause an electrical shock, smoke or fire when a capacitor is shorted, be sure to install fail-safe functions, such as a fuse, to prevent secondary accidents.

(2) This series are not safety standard certified products.

2-4. Remarks

Failure to follow the cautions may result, worst case, in a short circuit and smoking when the product is used. The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions.

Select optimum conditions for operation as they determine the reliability of the product after assembly. The data herein are given in typical values, not guaranteed ratings.

muRata Notice

Rating

1.Operating Temperature

- 1. The operating temperature limit depends on the capacitor.
- 1-1. Do not apply temperatures exceeding the maximum operating temperature. It is necessary to select a capacitor with a suitable rated temperature that will cover the operating temperature range. It is also necessary to consider the temperature distribution in equipment and the seasonal temperature variable factor.
- 1-2. Consider the self-heating factor of the capacitor The surface temperature of the capacitor shall not exceed the maximum operating temperature including self-heating.

2.Atmosphere Surroundings (gaseous and liquid)

- 1. Restriction on the operating environment of capacitors.
- 1-1. Capacitors, when used in the above, unsuitable, operating environments may deteriorate due to the corrosion of the terminations and the penetration of moisture into the capacitor.
- 1-2. The same phenomenon as the above may occur when the electrodes or terminals of the capacitor are subject to moisture condensation.
- 1-3. The deterioration of characteristics and insulation resistance due to the oxidization or corrosion of terminal electrodes may result in breakdown when the capacitor is exposed to corrosive or volatile gases or solvents for long periods of time.

3.Piezo-electric Phenomenon

1. When using high dielectric constant type capacitors in AC or pulse circuits, the capacitor itself vibrates at specific frequencies and noise may be generated. Moreover, when the mechanical vibration or shock is added to capacitor, noise may occur.

Others

1.Transportation

- 1. The performance of a capacitor may be affected by the conditions during transportation.
- 1-1. The capacitors shall be protected against excessive temperature, humidity and mechanical force during transportation.
 - (1) Climatic condition
 - low air temperature : -40°C
 - change of temperature air/air : -25°C/+25°C
 - · low air pressure : 30 kPa
 - · change of air pressure : 6 kPa/min.

(2) Mechanical condition

Transportation shall be done in such a way that the boxes are not deformed and forces are not directly passed on to the inner packaging.

- 1-2. Do not apply excessive vibration, shock, or pressure to the capacitor.
 - (1) When excessive mechanical shock or pressure is applied to a capacitor, chipping or cracking may occur in the ceramic body of the capacitor.
 - (2) When the sharp edge of an air driver, tweezers, a chassis, etc. impacts strongly on the surface of the capacitor, the capacitor may crack and short-circuit.
- 1-3. Do not use a capacitor to which excessive shock was applied by dropping etc. A capacitor dropped accidentally during processing may be damaged.

2.Characteristics Evaluation in the Actual System

- 1. Evaluate the capacitor in the actual system, to confirm that there is no problem with the performance and specification values in a finished product before using.
- 2. Since a voltage dependency and temperature dependency exists in the capacitance of high dielectric type ceramic capacitors, the capacitance may change depending on the operating conditions in the actual system. Therefore, be sure to evaluate the various characteristics, such as the leakage current and noise absorptivity, which will affect the capacitance value of the capacitor.
- 3. In addition, voltages exceeding the predetermined surge may be applied to the capacitor by the inductance in the actual system. Evaluate the surge resistance in the actual system as required.

- 1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. Your are requested not to use our product deviating from this product specification.
- 3.We consider it not appropriate to include any terms and conditions with regard to the business transaction in the product specifications, drawings or other technical documents. Therefore, if your technical documents as above include such terms and conditions such as warranty clause, product liability clause, or intellectual property infringement liability clause, they will be deemed to be invalid.