



RECOMMENDED PCB PATTERN

PART NUMBERS	CKT SIZE	DIM A	DIM B	DIM C	DIM D
87256-0447	04	(2.54)/.100	(12.70)/.500	(10.16)/.400	(7.62)/.300
87256-0647	06	(5.08)/.200	(15.24)/.600	(12.70)/.500	(10.16)/.400
87256-0847	08	(7.62)/.300	(17.78)/.700	(15.24)/.600	(12.70)/.500
87256-1047	10	(10.16)/.400	(20.32)/.800	(17.78)/.700	(15.24)/.600
87256-1247	12	(12.70)/.500	(22.86)/.900	(20.32)/.800	(17.78)/.700
87256-1447	14	(15.24)/.600	(25.40)/1.000	(22.86)/.900	(20.32)/.800
87256-1647	16	(17.78)/.700	(27.94)/1.100	(25.40)/1.000	(22.86)/.900
87256-1847	18	(20.32)/.800	(30.48)/1.200	(27.94)/1.100	(25.40)/1.000
87256-2047	20	(22.86)/.900	(33.02)/1.300	(30.48)/1.200	(27.94)/1.100
87256-2447	24	(27.94)/1.100	(38.10)/1.500	(35.56)/1.400	(33.02)/1.300
87256-2647	26	(30.48)/1.200	(40.64)/1.600	(38.10)/1.500	(35.56)/1.400
87256-3047	30	(35.56)/1.400	(45.72)/1.800	(43.18)/1.700	(40.64)/1.600
87256-3447	34	(40.64)/1.600	(50.80)/2.000	(48.26)/1.900	(45.72)/1.800
87256-4047	40	(48.26)/1.900	(58.42)/2.300	(55.88)/2.200	(53.34)/2.100
87256-4447	44	(53.34)/2.100	(63.50)/2.500	(60.96)/2.400	(58.42)/2.300
87256-5047	50	(60.96)/2.400	(71.12)/2.800	(68.58)/2.700	(66.04)/2.600

PART NUMBER LEGEND

87256 - • • 4 7

CKT SIZES

NOTES :

- PIN PUSHOUT FORCE (0.9072 KG)/2 LBS MIN.
- PRODUCT SPECIFICATION #70246 APPLIES.
- WAFER TO BE FLAT WITHIN (0.03MM/CM)/.003IN/IN.
- DIMENSIONS FOR PLATING LOCATIONS : A - MEASURE POINT FOR THICKNESS.  
B - MINIMUM COVERAGE.
- GOLD END OF PIN UNLESS OVERALL PLATED.
- FINISH : (PER ES-88)  
0.13um/5u" MIN GOLD IN SELECTED AREA,  
AND 1.9um/75u" MIN TIN/LEAD IN SELECTED AREA,  
BOTH OVER 1.27um/50u" MIN NICKEL OVERALL.
- FOR ILLUSTRATION PURPOSE A 10 CKT IS SHOWN.
- WEEK/YEAR STAMP NOT REQUIRED BY CUSTOMER.

(MM)  
INCH

				MATERIAL :		MOLEX SINGAPORE	
				WAFER : GLASS FILLED PBT		PTE LTD	
				PINS : (0.64)/.025 SO CU ALLOY		SHEET 1 OF 1	
				FINISH :		GENERAL TOLERANCES ± 0.20/.008	
				SEE NOTE 6		REV A3	
				DRAWN BY KC LING 940701		CHK'D BY ED POH 940712	
				APPR'D BY ROY WONG 940714		TITLE : C-GRID, SHROUDED WAFER SLOTTED, WITH IN-LINE POLARIZATION PEGS, WITH END WINDOWS	
REVISE ONLY ON CAD SYSTEM				DATE		SCALE 2 : 1	