

The following is a new Micron Part Change Notification. If you have questions regarding this email, please contact your sales representative, field support team or use the information request button in this email.

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DDR3 SPD Updates

PCN: 31806

Published: 2016-02-11

Type: SPD Revision

Description:

Modules of the -1G9 speed bin, built with 1Gb, 2Gb and 4Gb, x4 and x8 DDR3 devices will have the following change:

tFAWmin will be changed from 25ns to 27ns in SPD Byte 29 to reflect the correct JEDEC DDR3 tFAW timing for this speed bin. Please refer to the JEDEC DDR3 Specification for further information if needed.

Byte	Pre-PCN	Post-PCN
29	0xC8	0xD8
	(25ns)	(27ns)

DDR3 modules built on PCBs 1294, 1383, 1239 and 1348 are be updated to reflect the correct max heights previously indicated in Byte 60 of the SPD.

PCBs	Byte	Pre-PCN	Post-PCN
1294	60	0x10	0x1B (42mm max)
1383	60	0x10	Ox1B (42mm max)
1239	60	0x1B	0x1F (>45mm max)
1348	60	0x2F	0x0F (30mm max)

Anticipated Impact:

Customers may need to adjust incoming inspection to accommodate for changing SPD data. No functional impact

	expected.
Reason:	Better Align With Industry Standard
Product Affected:	Modules of the -1G9 speed bin, built with 1Gb, 2Gb and 4Gb, x4 and x8 DDR3 devices; all DDR3 LRDIMM modules built on PCBs 1294, 1383, and 1239; all DDR3 SODIMM modules built on PCB 1348.

Affected Micron Part Number	Recommended Replacement	Customer Part Number
<u>Module</u>		
MT16JTF1G64AZ-1G9D1		
MT16JTF1G64AZ-1G9J1		
MT16JTF51264AZ-1G9M1		
MT16KTF1G64AZ-1G9E1		
MT16KTF1G64AZ-1G9N1		
MT16KTF1G64AZ-1G9P1		
MT16KTF1G64HZ-1G9E1		
MT16KTF1G64HZ-1G9E2		
MT16KTF1G64HZ-1G9N2		
MT16KTF1G64HZ-1G9P1		
MT16KTF1G64HZ-1G9P2		
MT16KTF2G64AZ-1G9A1		
MT16KTF2G64HZ-1G9A1		
MT18JDF1G72PDZ-1G9E2		
MT18JDF1G72PDZ-1G9P1		
MT18JDF1G72PZ-1G9E1		
MT18JDF1G72PZ-1G9P1		
MT18JDF51272PDZ-1G9K2		
MT18JDF51272PZ-1G9K2		
MT18JSF1G72AZ-1G9E1		
MT18JSF1G72AZ-1G9E2		
MT18JSF1G72AZ-1G9E3		
MT18JSF1G72AZ-1G9J1		
MT18JSF1G72AZ-1G9P1		
MT18JSF1G72PDZ-1G9E1		
MT18JSF1G72PDZ-1G9E2		
MT18JSF1G72PDZ-1G9J1		
MT18JSF1G72PDZ-1G9N1		
MT18JSF1G72PDZ-1G9P1		

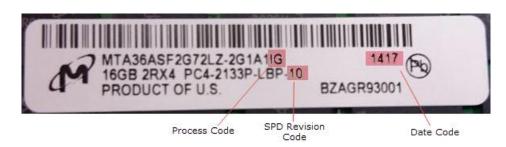
ME1010E1070E7 100E1
MT18JSF1G72PZ-1G9E1
MT18JSF1G72PZ-1G9J1
MT18JSF1G72PZ-1G9N1
MT18JSF1G72PZ-1G9P1
MT18JSF51272AZ-1G9K1
MT18JSF51272AZ-1G9N1
MT18JSF51272PDZ-1G9K1
MT18JSF51272PZ-1G9K1
MT18JSF51272PZ-1G9N1
MT18KSF1G72XDZ-1G9E1
MT18KSF51272XDZ-1G9K1
MT36JDZS2G72PZ-1G9E1
MT36JSF1G72PZ-1G9K1
MT36JSF1G72PZ-1G9N1
MT36JSF2G72PZ-1G9E1
MT36JSF2G72PZ-1G9E3
MT36JSF2G72PZ-1G9J1
MT36JSF2G72PZ-1G9N1
MT36JSF2G72PZ-1G9P1
MT36JSF4G72PZ-1G9A1
MT36KSF2G72PZ-1G9N1
MT4KTF25664AZ-1G9E1
MT4KTF25664AZ-1G9P1
MT4KTF25664HZ-1G9E1
MT4KTF25664HZ-1G9P1
MT4KTF25664HZ-1G9P2
MT72JSZS4G72LZ-1G9E2A7
MT72JSZS4G72LZ-1G9E2B3
MT72JSZS4G72LZ-1G9E2C3
MT72JSZS4G72LZ-1G9N1A7
MT72JSZS4G72LZ-1G9N1C3
MT72KGF4G72LZ-1G4E1A6
MT72KGF4G72LZ-1G6E2A8
MT72KGF4G72LZ-1G6J1A8
MT72KHF2G72LZ-1G4M1A6
MT72KHF4G72LZ-1G4D1A6
MT8JTF25664AZ-1G9M1
MT8KTF12864HZ-1G4J1

MT8KTF25664HZ-1G6N1	
MT8KTF51264AZ-1G9E1	
MT8KTF51264AZ-1G9N1	
MT8KTF51264AZ-1G9P1	
MT8KTF51264HZ-1G6N1	
MT8KTF51264HZ-1G6P1	
MT8KTF51264HZ-1G9E1	
MT8KTF51264HZ-1G9E2	
MT8KTF51264HZ-1G9E5	MT8KTF51264HZ-1G9E5- ND
MT8KTF51264HZ-1G9N1	
MT8KTF51264HZ-1G9N2	
MT8KTF51264HZ-1G9P1	
MT8KTF51264HZ-1G9P2	
MT9JSF25672AZ-1G9K1	
MT9JSF25672AZ-1G9N1	
MT9JSF25672PZ-1G9K1	
MT9JSF51272AZ-1G9E2	
MT9JSF51272AZ-1G9P1	
MT9JSF51272PZ-1G9E2	
MT9JSF51272PZ-1G9J1	

*Materials that have been ordered are in **bold**.

Method of Identification:

Work week 18, 2016. Label will show 1618. Please reference label example for location of date code.



Micron Sites Affected:

All Sites

Qualification Plan:

SPD Byte changes will be tested to insure industry standard BIOS's do not prevent system from booting. Functional issues are not anticipated as these changes are JEDEC standard and do not reflect any functional differences in the DRAM or other devices

	used on the module however, customer should evaluate theses changes as they may apply to their design and validate accordingly.
Sample Available:	2016-04-29
Qual Data Available:	2016-03-21
Product Ship Date:	2016-05-11

NOTE: Per JEDEC Standard JESD46-C Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change.

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Thank you

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