PCN Number		PCN Date: October 19, 2022						
Title: Qualification of additional Fab Site (RFAB) and Assembly Site (MLA) options for select devices								
Customer Co	ntact:	<u>PCN Manager</u>		Dept:	Quality Services			
Proposed 1 st		Jan 12, 2023	accept	e Requests ed until:	Nov 12, 2022*			
*Sample requests received after November 12, 2022 will not be supported.								
Change Type	:							
Assembly	/ Site	Assembly P	rocess	🛛 🖾 Asseı	mbly Materials			
Design		Electrical Sp	pecification	🗌 Mech	anical Specification			
Test Site		Packing/Shi	ipping/Labeling	Test	Test Process			
🗌 Wafer Bu	mp Site	🗌 🛛 Wafer Bum	o Material	🗌 🗌 Wafe	Wafer Bump Process			
🛛 🛛 Wafer Fa	b Site	🛛 🛛 Wafer Fab I	Materials	🗌 Wafe	Wafer Fab Process			
		Part numbe						
		PCN	Details					
Description	of Change:							
Revision A is	to update the A	ssembly Construc	tion difference:	s table in the D	<mark>escription of change</mark>			
<mark>section. The c</mark>	orrections are no	ted below and are	e in bold yello	<mark>ow highlight.</mark>				
Texas Instruments is pleased to announce the qualification of RFAB as an additional wafer fab site and MLA as additional assembly site options for the devices listed in the "Product Affected" section.								
	Current Fab S	ite	Additional Fab Site					
Current Fa	b Process	Wafer	Additional	Process	Wafer			
Site		Diameter	Fab Site		Diameter			
MIHO	LBC8	200 mm	RFAB	LBC8	300 mm			
		(5614	6 H					

Assembly construction differences/BOM options are as follows:

	TAI Current	New (TAI + MLA)
Bond wire diameter composition, diameter	Au, 0.8 0.96 mil	0.8 <mark>1</mark> mil Cu Die- > LF 0.8 <mark>0.96</mark> mil Au Die- >Die

Reason for Change:

Continuity of Supply

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
🛛 No Change	🛛 No Change	🛛 No Change	🛛 No Change

Chip Site		Chip Site Ori <u>c</u> Code (20L)		Chip Site Country C	Code (21L)	Chip Site City
MIHO8		MH8		JPN		Iba ra ki
RFAB		RFB		USA		Richardson
ssembly Site In	formati	ion:				
Assembly Site				embly Country Code (23L)	Assembly City	
TAI		TAI		TWN	Chung Ho, New Taipei Cit	
MLA		MLA		MNC	Kuala Lumpur	
mple product shi	pping la				Kual	a Lumpur
TEXAS INSTRUMENTS MADE IN: Malaysia 20: 20: MSL 2 /260C/1 YEAR MSL 1 /235C/UNLIM DPT: TEM:	G4	bel (not actual p	(1P (Q) (31 (4W (P) (2P) (29)		512 7	a Lumpur
TEXAS INSTRUMENTS MADE IN: Malaysia 2DC: 20: MSL 2 /260C/1 YEAR MSL 1 /235C/UNLIM DPT: TEM:	G4 SEAL DT 03/29/04 39 1750	bel (not actual p	(1P (Q) (31 (4W (P) (2P) (29)	t label)) SN74LS07NSR) 2000 (D) 0336 т) Lot: 3959047MLA) тку(1т) 7523483S REV: (V) 003331) ссо-сне (21) ссо-но	512 7	a Lumpur
TEXAS INSTRUMENTS MADE IN: Malaysia 20: 29: MSL '2 /260C/1 YEAR MSL 1 /235C/UNLIM DPT: TTEM: BL: 5A (L)TO	G4 seal dt 03/29/04 39 1750	bel (not actual p	(1P (Q) (31' (4W (P) (2P) (2P) (22)	t label)) SN74LS07NSR) 2000 (D) 0336 т) Lot: 3959047MLA) тку(1т) 7523483S REV: (V) 003331) ссо-сне (21) ссо-но	512 7	

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 08-SEPTEMBER-2022

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: UCC21732QDWQ1	Qual Device: UCC21737QDWQ1	Qual Device: UCC21750QDWQ1	QBS Reference: UCC21737QDWQ1	QBS Reference: <u>UCC23513QDWYQ1</u>	QBS Reference: ISO6741QDWQ1
Test Group /	oup A - Accelerated Environment Stress Tests												
PC	A1	JEDEC J- STD-020	3	77	Preconditioning	MSL2 260C	1 Step			-		No Fails	No Fails
		JESD22- A113											
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL3 260C	1 Step	-	No Fails	-	No Fails	-	-
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	1/77/0	-	1/77/0	3/231/0	3/231/0
AC/UHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours	-	1/77/0	-	1/77/0	3/231/0	3/231/0
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	1/77/0	-	1/77/0	3/231/0	3/231/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	1/5/0	-	1/5/0	1/5/0	1/5/0
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	1/77/0	-	1/77/0	-	3/135/0
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	-	3/135/0	-

Test Group	B - Acce	lerated Lifetime	e Simulat	tion Test	s.								
HTOL	В1	JEDEC JESD22- A108	1	77	Life Test	125C	1000 Hours	-	1/77/0	-	1/77/0	3/231/0	3/231/0
ELFR	B2	AEC Q100- 008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	-	-	3/2400/0	
Test Group	C - Pack	age Assembly	Integrity	Tests									
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	1/30/0	1/30/0	1/30/0	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	1/30/0	1/30/0	1/30/0	3/90/0	3/90/0
SD	СЗ	JEDEC JESD22- B102	1	15	PB Solderability	>95% Lead Coverage	-	-	-	-	-	1/15/0	1/15/0
SD	СЗ	JEDEC JESD22- B102	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	•	-	1/15/0	1/15/0
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	1/10/0	1/10/0	1/10/0	3/30/0	3/30/0
Test Group	D - Die	Fabrication Reli	iability To	ests									
ЕМ	D1	JESD61		-	Electromigration		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
нсі	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4		-	-	Negative Bias Temperature Instability		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
sм	D5		-	-	Stress Migration		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group	E - Elec	trical Verificatio	on Tests										
ESD	E2	AEC Q100- 002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004		1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	1/30/0	1/30/0	1/30/0	3/90/0	3/90/0
Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device	Qual Device	Qual Device	QBS Reference	QBS Reference	QBS Reference
				Lot									

QBS: Qual By Similarity

Qual Device UCC21732QDWQ1 is qualified at MSL3 260C

Qual Device UCC21737QDWQ1 is qualified at MSL3 260C

Qual Device UCC21750QDWQ1 is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- · Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

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