

<b>PCN Number:</b>	20170915000A	<b>PCN Date:</b>	Oct. 2, 2017
<b>Title:</b>	Datasheet for TPS549D22		
<b>Customer Contact:</b>	PCN Manager	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

**Notification Details**

**Description of Change:**

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**TPS549D22**

SLUSCI9A – AUGUST 2016 – REVISED SEPTEMBER 2017

**Changes from Original (August 2016) to Revision A**

**Page**

• Changed package name to correct "LQFN-CLIP" .....	1
• add links for WEBENCH .....	1
• Added MIN and MAX values for VDD UVLO rising threshold .....	5
• Added MIN and MAX for all Soft Start settings and table notes 3 and 4 in <i>Electrical Characteristics</i> .....	7
• Changed V <sub>OUT</sub> = 5 V to V <sub>OUT</sub> = 5.5 V for <b>Figure 24</b> .....	15
• Added note 2 for "Allowable Mode Selection" table .....	20
• Added <i>Application Workaround to Support 4-ms and 8-ms SS Settings</i> .....	20
• Added <b>Figure 27</b> and <b>Figure 28</b> .....	20
• Added "programmable" between "1-ms" and "delay" .....	22
• Changed 0, 1, 4 from "R/W" to "R" in ON_OFF CONFIG figure and table .....	28
• Deleted duplicative STATUS_BYTE table and figure .....	32
• Changed "286" to "2.86" in <i>Minimum Output Capacitance to Ensure Stability</i> subsection .....	46
• Changed "1.6 μs" to "1.538 μs"; "150 ns" to "300 ns"; and "963 μF" to "969 μF" in <i>Response to a Load Transient</i> .....	47

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS549D22	SLUSCI9	SLUSCI9A

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS549D22>

**Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):**

In order to ensure either 4-ms or 8-ms soft start setting would work properly in the customer system, additional application consideration is needed. The recommended application workaround to support the 4-ms and 8-ms soft-start settings is to ensure sufficient time delay between the VDD and EN<sub>UVLO</sub> signals. There are no changes to the actual device.

**Changes to product identification resulting from this PCN:**

None.

**Product Affected:**

TPS549D22RVFR	TPS549D22RVFT		
---------------	---------------	--	--

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
Japan	<a href="mailto:PCNJapanContact@list.ti.com">PCNJapanContact@list.ti.com</a>