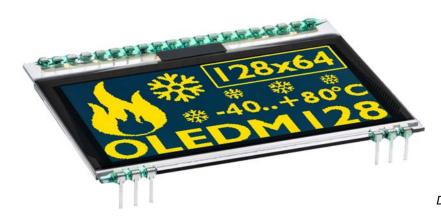




#### INCL CONTROLLER SSD1306 FOR SPI AND I2C



Dimension 55x46x3.3mm

#### **FEATURES**

- 128X64 DOTS (RELATES TO 8x21 CHARACTER OR 4x16 LARGE CHARACTER)
- HIGH CONTRAST OLED DISPLAY
- INTEGRATED CONTROLLER SSD1306
- SPI INTERFACE: MOSI, CLK, CS, D/C
- I2C INTERFACE: SDA, SCL
- WIDE TEMPERATURE RANGE (T<sub>OP</sub> -40°C +80°C)
- NO MOUNTING REQUIRED: JUST PUT INTO PCB
- 3 VERSIONS (WITH / W.O. POLARISOR AND PROTECTION GLASS) IN VARIOUS COLORS)
- FAST RESPONSE TIME, NO AFTERGLOW

#### ORDERING CODES

GRAPHIC 128x64, yellow, black background, incl. protection glass
 GRAPHIC 128x64, yellow, black background, w./o. protection glass
 GRAPHIC 128x64, white, black background, w./o. protection glass
 EA OLEDM128-6LGA
 EA OLEDM128-6LWA

Phone: +49 (0)8105-7780 90

e-Mail: info@lcd-module.com

Web: www.lcd-module.com

Fax: +49 (0)8105-7780 99

WITH A MINIMUM ORDER QTY. OF 10,000 PCS.

• GRAPHIC 128x64, with Polariser (Standard) EA OLEDM128-6LXA

X: G = Yellow

B = Blue

R = Red

## **ACCESSORIES**

TEST BOARD WITH USB-INTERFACE

SOCKET 4.8MM HOCH (2 PCS. ARE REQUIRED)

• TOUCHPANEL, 4-WIRE ANALOGUE SELF-ADHESIVE

ZIFF CONNECTOR FOR TOUCH, BOTTOM CONTACT

**EA 9781-1USB** 

EA FL-20P

EA TOUCH128-1\*)
EA WF100-04S

\*) IN COMBINATION WITH EA OLEDM128-6GGA ONLY





#### **EA OLED SERIES**

With its EA OLED series ELECTRONIC ASSEMBLY launched worldwide the first display family with OLED-technology for direct mounting and soldering. In comparison to standard displays there's no FFC/FPC cable/connector that may lose contact, this OLED series will be soldered directly or put into a standard 2.54 mm precision socket.

It is designed for compact handheld equipment and provides a lot real advantages:

- Extreme compact (55x46mm) with a large viewing area (51x31mm)
- Super flat with 2.1 mm (without frontal protection glass)
- SPI and I2C interface
- Simple mounting with direct soldering
- Ex stock available from 1 pc. off
- Long life time (up to 100,000 h are possible)
- Wide temperature range (-40..+80°C)
- Fast response time (10µs), no afterglow

#### **VERSIONS**

The EA OLEDM128-6 is available in 3 different versions:

#### EA OLEDM128-6GGA / Allround

This module is perfect for rough environment. An additional frontal glass protects the display against scratch, shock and UV light. Thanks to its integrated polariser there's no need for an additionally smoked glass.

#### EA OLEDM128-6LGA and -6LWA / Flat

This module is the standard module and does fit for the most applications. The flat design (2.4 mm) makes the display perfect for smallest equipment. The background is always deep black for best contrast.

## **COLORS (CUSTOM MADE)**

The standard colors are yellow and white.

The flat version EA OLEDM128-6LGA is on customers request available in 3 more colors. The minimum order quantity is 10,000 pcs. and lead time is about 20 weeks. Samples are available on request. Interface and software are 100% compatible. The yellow color provides highest brightness and longest life time.



EA OLEDM128-6LEA



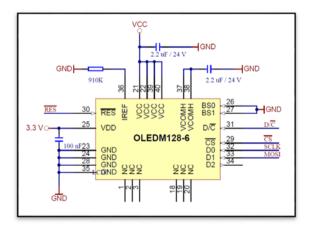
EA OLEDM128-6LRA



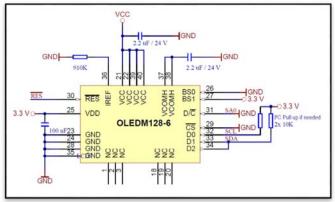
EA OLEDM128-6LBA

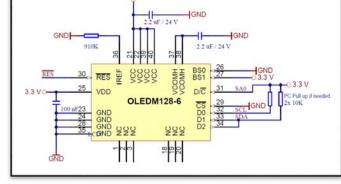


## **APPLICATION EXAMPLES**









VCC

2.2 uF / 24 V

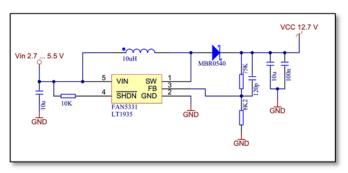
OLEDM128-6

20-19-8 20-19-8 20-19-8

30 RES

I<sup>2</sup>C Address 0x78 (0x3C)

I<sup>2</sup>C Address 0x7A (0x3D)



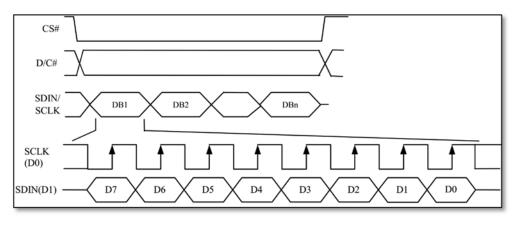
VCC - Generation: 12 V (FAN5331, LT1935)





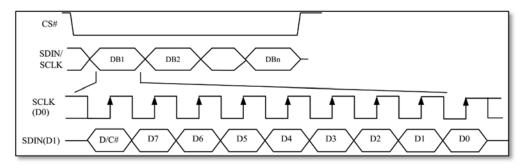
## **DATA TRANSFER 4-WIRE SPI (8 BIT)**

Data transmission for SPI is unidirectional, that means that data can only be written, there's no data read option. Selection for writing data or command is done with the D/C line. A busy check is not necessary at all. Clock rate may be up to 10 MHz. Data transmission is based on SPI mode 3, MSB first. For more details please refer to the controllers data sheet SSD1306.



## **DATA TRANSFER 4-WIRE SPI (9 BIT)**

Data transmission for SPI is unidirectional, that means that data can only be written, there's no data read option. Selection for writing data or command is done with the first bit of the 9 bit data transfer. A busy check is not necessary at all. Clock rate

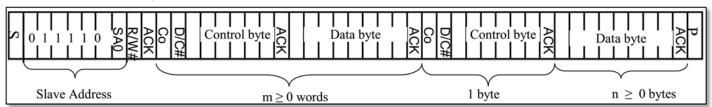


may be up to 10 MHz. Data transmission is based on SPI mode 3, MSB first (9 bit). For more details please refer to the controllers data sheet SSD1306.

### DATA TRANSFER I<sup>2</sup>C

The I<sup>2</sup>C mode provides a bi-directional data transmission: That means that data can be written and read. With the pin SA0 the I<sup>2</sup>C address can be changed, so up to 2 displays may be driven on 1 bus. The clock rate may be up to 400 KHz. Please make sure when defining the pull-up resistors that the internal resistance of the display is  $600..1000 \Omega$ . This affects the low level when reading data and ACK bit.

**Attention:** When reading data, after the command for page- or column address there need to be a dummy read (discard the first byte).



Control byte:  $C_o$  (Continuation bit) = 0  $\rightarrow$  Display data do follow; 1  $\rightarrow$  refer D/C bit





#### **GRAPHIC RAM**

The EA OLEDM128-6 comes with an integrated display RAM. Each byte represents 8 dots. For more details please refer to the controllers data sheet SSD1306, available on our website at

http://www.lcd-module.de/fileadmin/eng/pdf/zubehoer/SSD1306B 1.1.pdf.

# **COMMAND TABLE (ABSTRACT)**

This is a collection of the most important commands. The data sheet SSD1306 provides the full list plus a detailed description.

| 0             | Column address |
|---------------|----------------|
| D0<br>2<br>D7 | Page 0         |
| D0<br>2<br>D7 | Page 1         |
| D0<br>2<br>D7 | Page 2         |
| D0<br>D7      | Page 3         |
| D0<br>≥<br>D7 | Page 4         |
| D0<br>D7      | Page 5         |
| D0<br>≥<br>D7 | Page 6         |
| D0<br>D7      | Page 7         |

| Command        | D/ | Command Code |                |                |                       |                       |                       |                | Description           |                |  |
|----------------|----|--------------|----------------|----------------|-----------------------|-----------------------|-----------------------|----------------|-----------------------|----------------|--|
| Command        | С  | Hex          | D7             | D6             | D5                    | D4                    | D3                    | D2             | D1                    | D0             | Description  |
| Contrast       | 0  | 81           | 1              | 0              | 0                     | 0                     | 0                     | 0              | 0                     | 1              | Double byte command to select 1  |
| Control        | 0  | 7F           | A <sub>7</sub> | A <sub>6</sub> | <b>A</b> <sub>5</sub> | $A_4$                 | $A_3$                 | $A_2$          | A <sub>1</sub>        | $A_0$          | out of 256 contrast steps. Contrast increases as the value increases.            |
| Display        | 0  | AE/          | 1              | 0              | 1                     | 0                     | 1                     | 1              | 1                     | X <sub>0</sub> | X <sub>0</sub> =0: Display OFF (sleep mode)                                      |
| On / Off       |    | AF           |                |                |                       |                       |                       |                |                       |                | (RESET)  |
| Set            | 0  | 21           | 0              | 0              | 1                     | 0                     | 0                     | 0              | 0                     | 1              | X <sub>0</sub> =1: Display ON in normal mode Setup column start and end          |
| Column         | Ö  | 0            | $A_7$          | $A_6$          | A <sub>5</sub>        | $A_4$                 | A <sub>3</sub>        | $A_2$          | A <sub>1</sub>        | $A_0$          | address  |
| address        | 0  | 7F           | B <sub>7</sub> | B <sub>6</sub> | B <sub>5</sub>        | B <sub>4</sub>        | $B_3$                 | B <sub>2</sub> | B <sub>1</sub>        | $B_0$          | A[7:0] : Column start address,   |
|                |    |              |                |                |                       |                       |                       |                |                       |                | range : 0-127d, (RESET = 0)<br>B[7:0]: Column end address, range                 |
|                |    |              |                |                |                       |                       |                       |                |                       |                | : 0-127d, RESET = 127)   |
|                |    |              |                |                |                       |                       |                       |                |                       |                | Note: This command is only for   |
|                |    |              |                |                |                       |                       |                       |                |                       |                | horizontal or vertical addressing mode.  |
| Set Page       | 0  | 22           | 0              | 0              | 1                     | 0                     | 0                     | 0              | 1                     | 0              | Setup page start and end address   |
| address        | 0  | 0            | X              | X              | X                     | X                     | X                     | $A_2$          | <b>A</b> <sub>1</sub> | $A_0$          | A[2:0]: Page start Address, range  |
|                | 0  | 7            | X              | Х              | Х                     | X                     | X                     | B <sub>2</sub> | B <sub>1</sub>        | B <sub>0</sub> | : 0-7d, (RESET = 0)<br>B[2:0] : Page end Address, range :                        |
|                |    |              |                |                |                       |                       |                       |                |                       |                | 0-7d, (RESET = 7)  |
|                |    |              |                |                |                       |                       |                       |                |                       |                | Note: This command is only for horizontal or vertical addressing                 |
|                |    |              |                |                |                       |                       |                       |                |                       |                | mode.  |
| Display        | 0  | 40           | 0              | 1              | <b>A</b> <sub>5</sub> | <b>A</b> <sub>4</sub> | <b>A</b> <sub>3</sub> | A <sub>2</sub> | A <sub>1</sub>        | A <sub>0</sub> | Set display RAM display start line   |
| Start Line     |    | _            |                |                |                       |                       |                       |                |                       |                | register from 0-63 using $X_5X_3X_2X_1X_0$ .                                     |
|                |    | 7F           |                |                |                       |                       |                       |                |                       |                | Display start line register is reset to  |
|                |    |              |                |                |                       |                       |                       |                |                       |                | 0 during RESET.  |
| Segment        | 0  | A0/          | 1              | 0              | 1                     | 0                     | 0                     | 0              | 0                     | $X_0$          | X <sub>0</sub> =0: column address 0 is mapped to SEG0 (RESET)                    |
| remap          |    | A1           |                |                |                       |                       |                       |                |                       |                | $X_0$ =1: column address 127 is  |
|                |    |              |                |                |                       |                       |                       |                |                       |                | mapped to SEG0   |
| Com            | 0  | C0/          | 1              | 1              | 0                     | 0                     | <b>X</b> <sub>3</sub> | 0              | 0                     | 0              | X <sub>3</sub> =0: normal mode (RESET) Scan from COM0 to COM[N –1]               |
| output<br>scan |    | C8           |                |                |                       |                       |                       |                |                       |                | X <sub>3</sub> =1: remapped mode. Scan from                                      |
| direction      |    |              |                |                |                       |                       |                       |                |                       |                | COM[N-1] to COM0   |
|                | 1  | XX           | D              | <u> </u>       | <u> </u>              | D                     | D                     | <u> </u>       | D                     | D              | Where N is the Multiplex ratio D <sub>7</sub> -D <sub>0</sub> is written to RAM. |
| RAM Data       | 1  | ۸۸           | D <sub>7</sub> | D <sub>6</sub> | $D_5$                 | D <sub>4</sub>        | D <sub>3</sub>        | $D_2$          | D <sub>1</sub>        | $D_0$          | D7-D0 IS WITHEIT TO KAIVI.   |



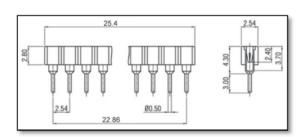


#### INITIALISATION EXAMPLE

```
void init_OLEDM128(void)
{
  send command(0x40);
                                              //Set Display start line
  send command(0 \times A0);
                                              //Bottom View no Segment remap
  send_command(0xC0);
                                              //Bottom View COM scan direction normal
  send command(0 \times A6);
                                              //Display normal (RAM)
  send_command(0x81); send_command(0xFF);
                                              //Set contrast to maximum
  send_command(0xD5); send_command(0x40);
                                              //Clock divider/Oscillator frequency
  send_command(0xD9); send_command(0x44);
                                              //Pre-charge Period
  send_command(0xAF);
                                              //Display on
}
```

#### ACCESSORY: SOCKET EA FL-20P

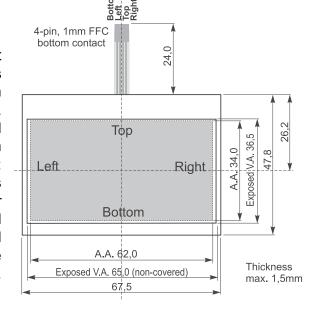
Using a 20-pin socket makes the display replaceable and adapts the height. Those socket may also be soldered automatically by wave soldering or reflow process. Each display requires 2 pcs. Also available in SMT (EA FL-20PS).

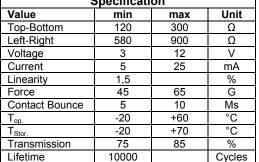


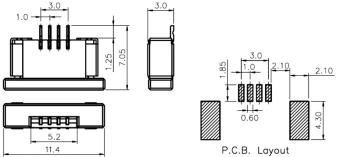
## **ACCESSORY: TOUCHPANEL EA TOUCH128-1**

As an accessory there is an analogue touchpanel available. It comes with a self-adhesive glue on its rear side. Connection is done via FFC, pitch 1.0 mm. Any standard ZIFF connector can be used (e.g. EA WF100-04S). Bending radius is minimum 5 mm. Interfacing to a processor can be either done by an external touch panel controller or with a controller that is featured with neter: nakes linear e will and of the wing.

| analogue inpu  | t. The to   | uch pan   | el is sir | milar t  | o a pote | entiom    |  |  |  |  |  |
|--|-------------|-----------|-----------|----------|----------|-----------|--|--|--|--|--|
| connecting a v   | oltage of   | e.g. 3.3  | V to the  | pins '   | Top-Bot  | tom m     |  |  |  |  |  |
| it possible to re  | ad out a    | voltage o | n pin Le  | eft or F | Right wh | ich is li |  |  |  |  |  |
| to the Y-coord   | inate of    | the pres  | sed poi   | nt. Th   | e X-coo  | rdinate   |  |  |  |  |  |
| to the Y-coordinate of the pressed point. The X-coordinate<br>result when the voltage will be supplied to Left-Right |             |           |           |          |          |           |  |  |  |  |  |
| measurement is done at Top or Bottom. The pinout of  |             |           |           |          |          |           |  |  |  |  |  |
|  | •           |           |           |          |          |           |  |  |  |  |  |
| Only in combination with the EA OLEDM128-6GGA.   |             |           |           |          |          |           |  |  |  |  |  |
| -  |             |           |           | 1        |          |           |  |  |  |  |  |
|  | Specificati | on        |           |          |          |           |  |  |  |  |  |
| Value  | min         | max       | Unit      |          |          |           |  |  |  |  |  |
| Top-Bottom   | 120         | 300       | Ω         |          |          | 1         |  |  |  |  |  |
| Left-Right   | 580         | 900       | Ω         |          |          | -         |  |  |  |  |  |
| Voltage  | 3           | 12        | V         | 1        |          | f         |  |  |  |  |  |
| Current  | 5           | 25        | mA        | 1        |          |           |  |  |  |  |  |
|  |             |           |           | 1        |          | - 11      |  |  |  |  |  |







## ACCESSORY: ZIFF CONNECTOR EA WF100-04S

The ZIFF connector matches perfect to the touch panel EA TOUCH128-1. It provides 4 pins with 1.0 mm pitch. Connection is bottom contact. The top contact version is called EA WF100-04T.





# **SPECIFICATION**

Unless otherwise specified, VSS = 0V , VDD = 1.8 - 3.3V ( Ta = 25°C)

| Value                              | Condition                  | min       | typ  | max       | Unit |
|------------------------------------|----------------------------|-----------|------|-----------|------|
| Operating Temperature              |                            | -40       |      | +80       | °C   |
| Storage Temperature                |                            | -40       |      | +80       | °C   |
| Storage Humidity                   | <40°C                      |           |      | 90        | %RH  |
| Operating Voltage VDD logic supply |                            | 1.8       | 3.0  | 3.3       | V    |
| Operating Voltage VCC OLED supply  |                            |           | 12.5 | 13.0      | V    |
| High Logic input level             |                            | 0.8 x VDD |      |           |      |
| Low Logic input level              |                            |           |      | 0.2 x VDD | V    |
|                                    | All Pixel off              |           | 0,4  |           | mA   |
| Power Supply VCC <sup>1)</sup>     | Demo picture <sup>2)</sup> |           | 12   |           | mA   |
|                                    | All Pixel on               |           | 28   |           | mA   |

<sup>&</sup>lt;sup>1)</sup> VCC= 12V, initialization 0x81,0xFF / 0xD5,0x40 / 0xD9,0x44 / 0xDB,0x20

<sup>2)</sup> Demo Picture:



## **OPTICAL DATA**

| Item                        | Symbol | Condition       | min    | typ  | max  | Unit  |  |
|-----------------------------|--------|-----------------|--------|------|------|-------|--|
| View Angle                  | (V)θ   | CR≧2000         | 160    | 170  |      | deg   |  |
| View Angle                  | (Η)φ   | CR≧2000         | 160    | 170  |      | deg   |  |
| Contrast Ratio CR           |        | Dark Room       | 2000:1 |      |      |       |  |
| Response Time               | T rise |                 |        | 10   |      | μs    |  |
| Response Time               | T fall |                 |        | 10   |      | μs    |  |
| Luminance <sup>1)</sup> L - |        | -6GGA / -6LGA   | 90     | 100  |      | cd/m² |  |
| CIE 1931 x(Yellow)          |        | Dark Room       | 0.45   | 0.47 | 0.49 |       |  |
| CIE 1931 y(Yellow)          |        | Dark Room       | 0.48   | 0.50 | 0.52 |       |  |
| Operating                   | yellow | 50% chess board | 50,000 |      |      | hrs   |  |
| Life Time <sup>2)</sup>     | white  | 50% chess board | 20,000 |      |      |       |  |

<sup>1)</sup> VDD= 3.3V, incl. booster 1) VDD= 3.3V, incl. booster FAN5331 with VCC= 12.5V

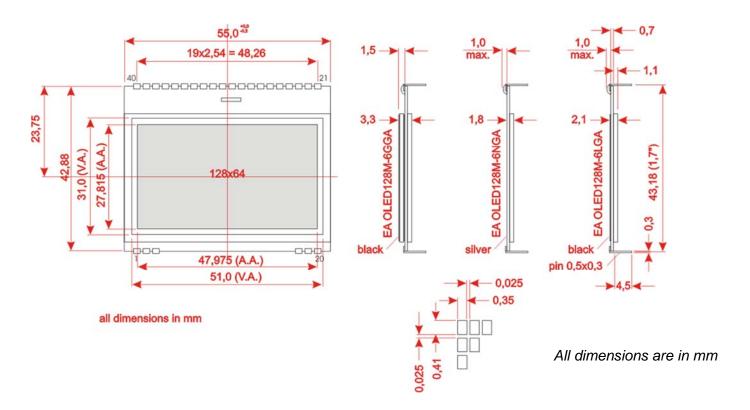
Printing and typographical errors reserved. ELECTRONIC ASSEMBLY reserves the right to change specifications without prior notice.

 $<sup>^{2)}</sup>$   $T_a$ =25°C, operating life time is defined the amount of time until the luminance has decayed to 50% of the initial value. Screen saving mode is recommended to extend life time





# **DIMENSIONS EA OLEDM128-6**



| Pin | Symbol  | Pin | Symbol                             | Description                                       |
|-----|---------|-----|------------------------------------|---|
| 1   | ESD_GND | 21  | VCC Typ. 12 V OLED driving voltage |   |
| 2   | NC      | 22  | VCC                                | Typ. 12 V OLLD driving Voltage                    |
| 3   | NC      | 23  | GND                                | Ground  |
| 4   |         | 24  | GND                                | Cround  |
| 5   |         | 25  | VDD                                | Typ. 3.3 V logic power supply                     |
| 6   |         | 26  | BS0                                | 00 = 4-Wire SPI; 01 = 3-Wire SPI                  |
| 7   |         | 27  | BS1                                | 10 = I <sup>2</sup> C Interface                   |
| 8   |         | 28  | GND                                | Ground  |
| 9   |         | 29  | cs                                 | Chip Select (active low)                          |
| 10  |         | 30  | RES                                | Reset (active low)                                |
| 11  |         | 31  | D/C                                | SPI (4-Wire): L=Command, H=Data, I2C: SA0         |
| 12  |         | 32  | D0                                 | SPI: SCLK, I <sup>2</sup> C: SCL                  |
| 13  |         | 33  | D1                                 | SPI: MOSI, I <sup>2</sup> C: SDA <sub>in</sub>    |
| 14  |         | 34  | D2                                 | SPI: NC, I <sup>2</sup> C: SDA <sub>out</sub>     |
| 15  |         | 35  | GND                                | Ground  |
| 16  |         | 36  | Iref                               | Reference for current source for segement drivers |
| 17  |         | 37  | VCOMH                              | Common deselect level. (Internally regulated)     |
| 18  | NC      | 38  | VCOMH                              |   |
| 19  | NC      | 39  | VCC Typ. 12 V OLED driving voltage |   |
| 20  | ESD_GND | 40  | VCC                                | 1.7p. 12 1 GLED diffing foliage                   |

- OLED displays are generally not suited for wave or reflow soldering. Temperatures of over 80°C can cause lasting damage.
- The surfaces of the displays are protected from scratching by self-adhesive protective foil. Please remove before mounting

