

Features

- AEC-Q101 Qualified
- Trench Power LV MOSFET Technology
- High Density Cell Design for Low $R_{DS(ON)}$
- High Speed Switching
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free."Green"Device⁽¹⁾
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

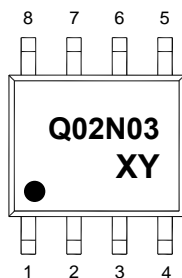
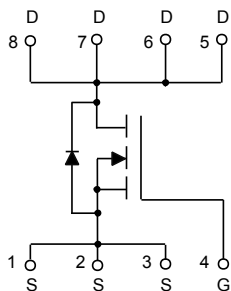
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 42°C/W Junction to Ambient ⁽²⁾

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	V_{GS}	±20	V	
Continuous Drain Current	I_D	$T_A=25^\circ C$	20	A
		$T_A=70^\circ C$	15.5	A
Pulsed Drain Current ⁽³⁾	I_{DM}	72	A	
Total Power Dissipation	P_D	3	W	

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Device Mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.
- 3.Pulse Test: Pulse Width≤300μs,Duty Cycle ≤2%.

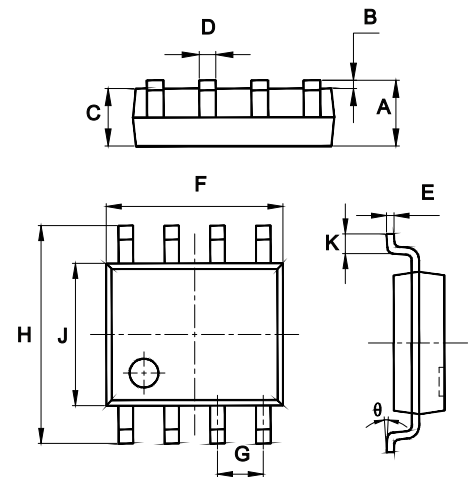
Internal Structure and Marking Code



2 codes in total
X is the year
Y is the month

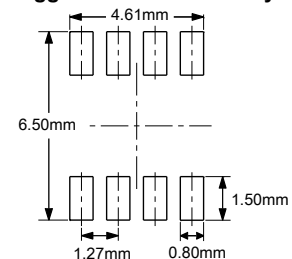
**N-CHANNEL
MOSFET**

SOP-8



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.35	1.75	
B	0.004	0.010	0.10	0.25	
C	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
H	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=18A$		4.5	6.3	m Ω
		$V_{GS}=4.5V, I_D=10A$		6.3	9	m Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				20	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=12A$		0.85	1.2	V
Reverse Recovery Time	t_{rr}	$I_S=20A, di/dt=100A/\mu s$		11		ns
Reverse Recovery Charge	Q_{rr}			1.6		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V, f=1MHz$		2191		pF
Output Capacitance	C_{oss}			300		
Reverse Transfer Capacitance	C_{rss}			247		
Total Gate Charge	Q_g	$V_{DS}=15V, V_{GS}=10V, I_D=20A$		46.3		nC
Gate-Source Charge	Q_{gs}			8.8		
Gate-Drain Charge	Q_{gd}			9.2		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=15V, R_L=0.75\Omega, R_{GEN}=3\Omega$		11		ns
Turn-On Rise Time	t_r			80		
Turn-Off Delay Time	$t_{d(off)}$			39		
Turn-Off Fall Time	t_f			92		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

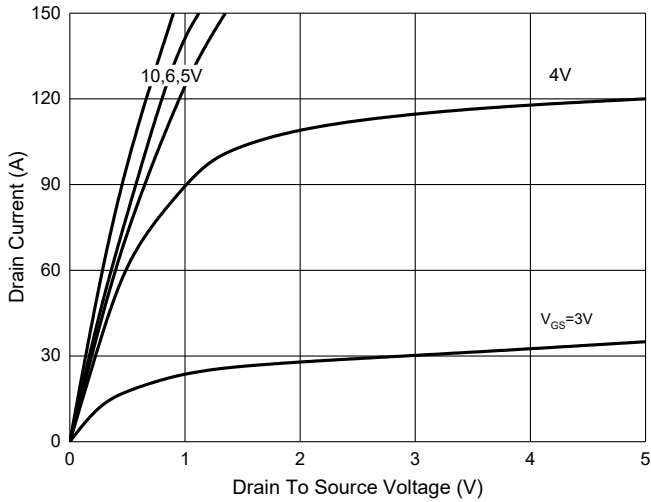


Fig. 2 - Transfer Characteristics

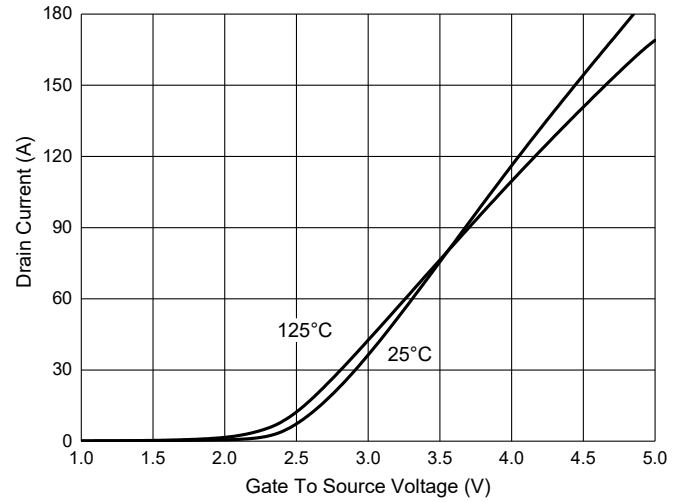


Fig. 3 - $R_{DS(ON)} - I_D$

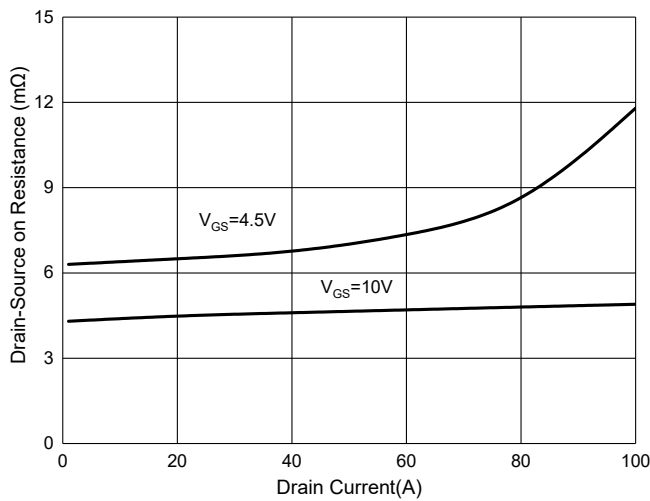


Fig. 4 - Normalized On Resistance Characteristics

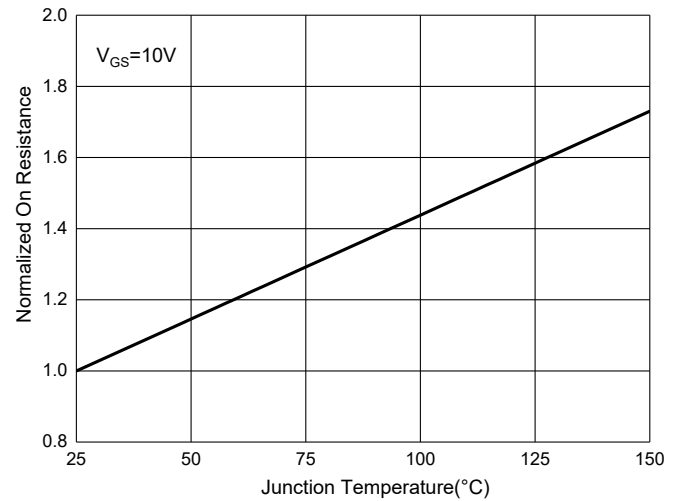


Fig. 5 - Capacitance Characteristics

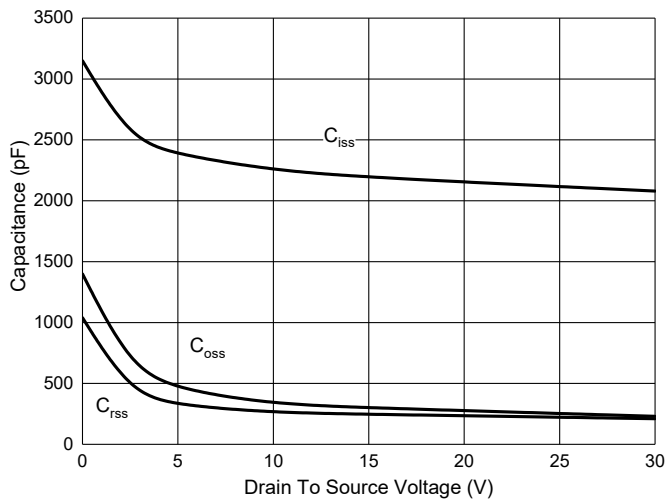
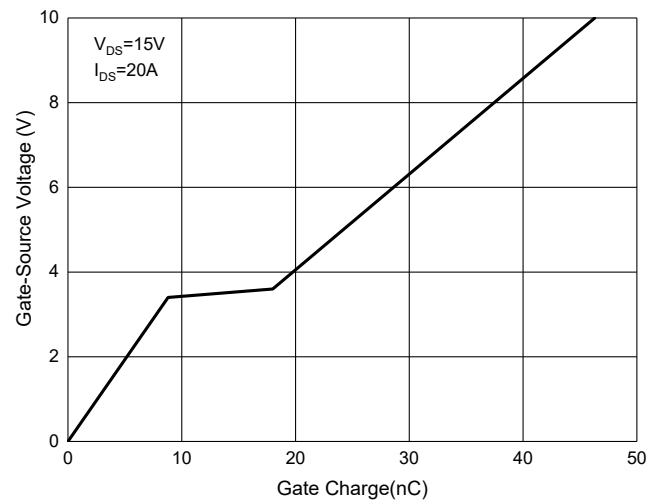


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area

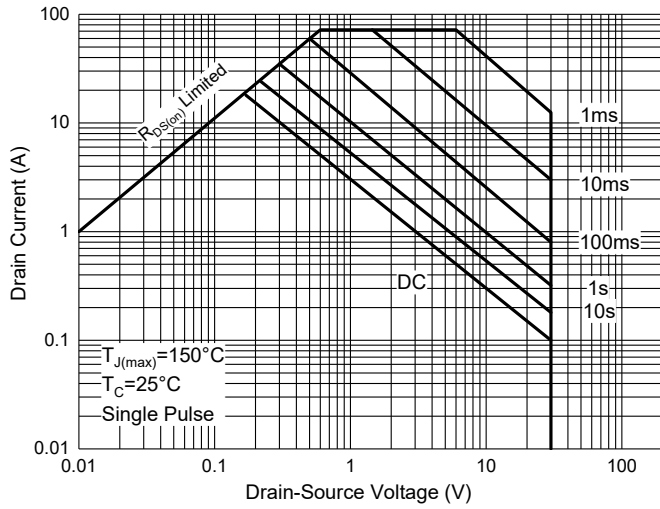
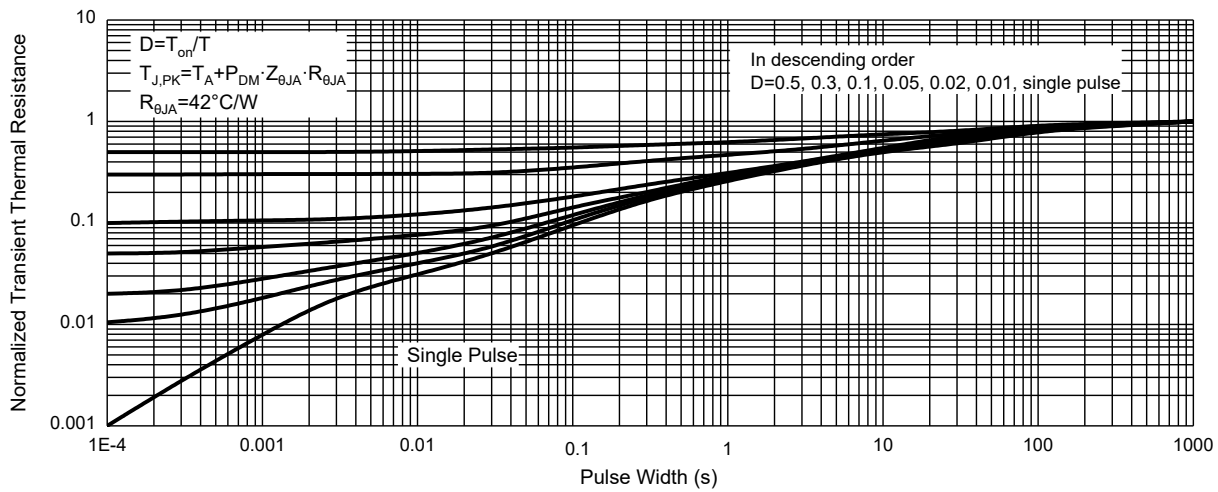


Fig. 8 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

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